




K. S. INSTITUTE OF TECHNOLOGY

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Faculty Name	DIVYA.D	
Designation	Assistant Professor	
Educational Qualification	BE, M.Tech	
Experience in Years	6	
Areas of Interest	VLSI	
E-mail	Divyad.d820@gmail.com	

Educational Details

Examination/ Degree	College / University	Year of Passing
Ph. D	-	-
M.Tech.	NMAMIT NITTE KARKALA	2013
B. E	CANARA ENGINEERING COLLEGE	2007

Publications

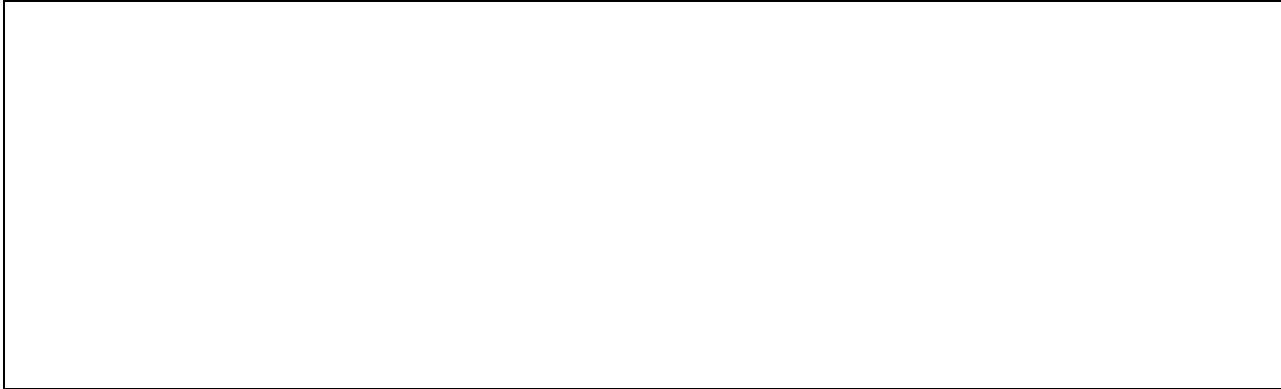
Journal Publications:

FPGA IMPLEMENTATION OF A DISTRIBUTED CANNY EDGE DETECTOR
1DIVYA.D, 2SUSHMA P.S Assistant Professor, Department of Electronics and Communication, NMAMIT, Nitte

Proceedings of IRAJ International Conference, 17th November 2013, Singapore, India. ISBN: 978-93-82702-41-2

Conference Papers:

Patents Filed:



Awards
Professional Membership
Contact Details
9535657022