K.S. INSTITUTE OF TECHNOLOGY, BANGALORE - 560109
DEARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TEACHING AND LEARNING

Details of Content beyond syllabus Activities-2023-24 (ODD)

7th Semester

Semester/ Section	Course Name	Content beyond syllabus activity conducted	POs Covered	Faculty	Number of Activity
VII A &B	Computer Networks 18EC71	Literature Survey	9, 10, 12	Dr. Dinesh Kumar D S	28
VII A &B	VLSI Design 18EC72	Poster Presentation	9, 10	Mr. Praveen A	24
VII A	Satellite Communication 18EC732	Poster Presentation	5, 9, 10, 12	Mr. Saleem S Tevaramani	15
VII B	Satellite Communication 18EC732	Poster Presentation	5, 9, 10, 12	Mr. Saleem S Tevaramani	15
VII A &B	Cryptography 18EC744	Mini Project	1,2,3,5,7,9,10,11,12	Dr. P N Sudha	112
VII A &B	Energy and Environment 18ME751	PPT Presentation	9,10,12	Mrs. Vishalini Divakar	30

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5th semester:

Semester/ Section	Course Name	Content beyond syllabus activity conducted	POs Covered	Faculty	Number of Activity
V/A&B	Digital Communication 21EC51	Poster presentation	1, 2, 9, 10, 11	Dr. Rekha N	36
V/A	Computer Organization & ARM Microcontroller 21EC52	Mini Projects	6,7,9,10,11,12	Dr. Sudarshan B	30
V /B	Computer Communication Networks 21EC53	Model making	1,2,5,9,10	Mrs. Sangeetha V	33
V/A	Electromagnetics Waves 21EC54	Poster Presentation	9,10,12	Mrs. Bhanumathi A	17
V/B	Electromagnetics Waves 21EC54	Poster Presentation	9,10,12	Mrs. Kavya B M	16
V/A	Research Methodology & Intellectual Property Rights 21ARI56	Literature review and case study	9	Dr. Electa Alice Jayarani A	17
V/B	Research Methodology & Intellectual Property Rights 21EC56	Literature Review and case studies	9	Mrs. Radhika T S	16
V/A&B	Environmental Studies 21CIV57	Group Discussion	6,7,9,10	Mrs. Radhika	13

Details of Content beyond syllabus Activities-2023-24 (ODD)

3rd Semester

Semester/ Section	Course Name	Content beyond syllabus activity conducted	POs Covered	Faculty	Number of Activity
III/A/B	AV Mathematics-III for EC Engineering BMATEC301	Project Presentation	9,10,12	Dr. Jalaja P Mr. Naveen	29
III/A	Digital System Design using Verilog BEC302	Code Debugging	4,10	Mr. Naveen Kumar S	68
III/B	Digital System Design using Verilog BEC302	Code Debugging	4,10	Mrs . Bhargavi Ananth	66
III/A&B	Electronic Principles and Circuits BEC303	Multi sim Lab	9,10,12	Dr. Devika B	112
III/A&B	Network Analysis BEC304	Network Simulation	1,2,3,5,7,9,10,11,12	Mr. Christo Jain	30
III/A	Computer Organization and Architecture BEC306C	Poster presentation	9,10,12	Mrs. Anita P	19
III/B	Computer Organization and Architecture BEC306C	Poster presentation	9,10,12	Mrs. Priyadharshini V	17