## **CRITERIA 3 ANNEXURES**

## **ANNEXURE 3.1.1**

## **EARLIER COs**

**Course: DSP** 

CO1	<b>Construct</b> the frequency domain sampling and reconstruction of discrete time signals.	Applying K3
CO2	<b>Make use of</b> the properties and develop efficient algorithms for the computation of DFT.	Applying K3
CO3	Construct FIR filters in different structural forms.	Applying K3
CO4	Utilize the procedures to design IIR filters from the analog filters using impulse invariance and bilinear transformation.	Applying K3
CO5	<b>Make use of</b> the characteristics of DSP processor and implement FIR and IIR filters.	Applying K3

## **REDEFINED COs**

**Course Name: DSP** 

18EC52.1	Utilize the frequency domain sampling and apply it on reconstruction of discrete time signals.	Applying K3
18EC52.2	<b>Identify</b> the properties of DFT and <b>develop</b> efficient algorithms for the computation of DFT and FFT.	Applying K3
18EC52.3	Construct FIR filters and apply it in different structural forms.	Applying K3
18EC52.4	Utilize the procedures of IIR filters and develop IIR filters from the analog filters using impulse invariance and bilinear transformation.	Applying K3
18EC52.5	Make use of the architecture of DSP processor and apply it on FIR and IIR filters.	Applying K3