



K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 31/10/2022

SEC : 'A'

CLASS TEACHER : Mr. Santhosh Kumar B R

CLASS ROOM : OB LH 201

PERIOD	1	2	10.20 AM	3	4	12.25 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM	10.20 AM 10.35 AM	10.35 AM 11.30 AM	11.30 AM 12.25 PM	12.25 PM 1.15 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	BSP (21EC33)	DSDV (21EC32)	T E A B R E	AEC (21EC34)	SC&R (21UH36)	L U N C H B R E A K	Kannada (21KSK37/21KKB37)	BSP (21EC33)(T)	(T)
TUE	DSDV (21EC32)	MATHS (21MAT31)		BSP (21EC33)	AEC (21EC34)		NSS (21NS83) / PE (21PE83) / YOGA (21YO83)		
WED	MATHS (21MAT31)	DSDV (21EC32)		AEC (21EC34)	BSP (21EC33)		A&DE LAB (21ECL35)-A1 / ← AEC Lab using Pspice MultiSIM (21EC382) / → BSP LAB(21EC33)-A2/DSDV LAB (21EC32)-A3		
THU	← A&DE LAB (21ECL35)-A3 / AEC Lab using Pspice MultiSIM (21EC382)/BSP LAB(21EC33)-A1/ DSDV LAB (21EC32)-A2 →				MATHS (21MAT31)		AEC (21EC34)(T)	DSDV (21EC32)(T)	(T)
FRI	AEC (21EC34)	BSP (21EC33)	A K	MATHS (21MAT31)	DSDV (21EC32)		← A&DE LAB (21ECL35)-A2 / AEC Lab using Pspice MultiSIM (21EC382) / → BSP LAB(21EC33)-A3/DSDV LAB (21EC32)-A1		

Sub-Code	Subject Name	Faculty Name
21MATDIP31	Additional Mathematics - I	
21MAT31	Mathematics Course (Common to all)	Mrs. Mamatha N
21EC32	Digital System Design using Verilog	Mr. Santhosh Kumar B R
21EC33	Basic Signal Processing	Mrs. Anita P
21EC34	Analog Electronic Circuits	Dr. Chanda V Reddy
21ECL35	Analog & Digital Electronics Lab	Dr. Chanda V Reddy, Mr. Sampath Kumar Mrs. Vishalini Divakar (A2)
21UH36	Social Connect and Responsibility	Mr. Sampath Kumar
21KSK37/47	Sanskrutika Kannada	Mr. Thrimurthy
21KKB37/47	Balake Kannada	
21EC382	AEC Lab using Pspice / MultiSIM (Ability Enhancement Course - III)	Mr. Christo Jain , Mrs. Anita P
21EC32	Digital System Design using Verilog Lab	Mr. Santhosh Kumar B R , Mrs. Suma Santosh
21EC33	Basic Signal Processing Lab	Mr. Christo Jain , Mrs. Anita P
21NS83	National Service Scheme (NSS)	
21PE83	Physical Education (PE)(Sports and Athletics)	
21YO83	Yoga	

V. S. [Signature]
Time Table Co-ordinator

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
III SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 31/10/2022
 SEC : 'B'

CLASS TEACHER : Mr. Christo Jain
 CLASS ROOM : OB LH 203

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	AEC (21EC34)	DSDV (21EC32)	T E A B R E A K	BSP (21EC33)	Kannada (21KSK37/21KKB37)	L U N C H B R E A K	A&DE LAB (21ECL35)-B1 / ← AEC Lab using Pspice MultiSIM (21EC382)/ → BSP LAB(21EC33)-B2/DSDV LAB (21EC32)-B3		
TUE	BSP (21EC33)	AEC (21EC34)		DSDV (21EC32)	MATHS (21MAT31)		A&DE LAB (21ECL35)-B3 / ← AEC Lab using Pspice MultiSIM (21EC382)/ → BSP LAB(21EC33)-B1/DSDV LAB (21EC32)-B2		
WED	AEC (21EC34)	BSP (21EC33)		MATHS (21MAT31)	DSDV (21EC32)(T)		NSS (21NS83) /PE (21PE83)/YOGA (21YO83)		
THU	DSDV (21EC32)	MATHS (21MAT31)		BSP (21EC33)(T)	SC&R (21UH36)		A&DE LAB (21ECL35)-B2 / ← AEC Lab using Pspice MultiSIM (21EC382)/ → BSP LAB(21EC33)-B3/DSDV LAB (21EC32)-B1		
FRI	BSP (21EC33)	DSDV (21EC32)		AEC (21EC34)	MATHS (21MAT31)		AEC (21EC34)(T)	(T)	DIP MATHS (21MATDIP31)

Sub-Code	Subject Name	Faculty Name
21MATDIP31	Additional Mathematics - I	Mr. Naveen V
21MAT31	Mathematics Course (Common to all)	Mr. Naveen V
21EC32	Digital System Design using Verilog	Mrs. Suma Santosh
21EC33	Basic Signal Processing	Mr. Christo Jain
21EC34	Analog Electronic Circuits	Dr. Chanda V Reddy
21ECL35	Analog & Digital Electronics Lab	Dr. Chanda V Reddy(B1) , Mr. Sampath Kumar Mrs. Vishalini Divakar
21UH36	Social Connect and Responsibility	Mr. Sampath Kumar
21KSK37/47	Samskrutika Kannada	Mr. Thrimurthy
21KKB37/47	Balake Kannada	
21EC382	AEC Lab using Pspice / MultiSIM (Ability Enhancement Course - III)	Mr. Christo Jain , Mrs. Anita P
21EC32	Digital System Design using Verilog Lab	Mr. Santhosh Kumar B R , Mrs. Suma Santosh
21EC33	Basic Signal Processing Lab	Mr. Christo Jain , Mrs. Anita P
21NS83	National Service Scheme (NSS)	
21PE83	Physical Education (PE)(Sports and Athletics)	
21YO83	Yoga	

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V SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 10/10/2022

SEC : 'A'

CLASS TEACHER : Mrs.Sangeetha.V

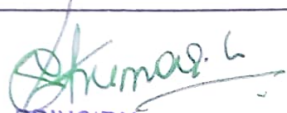
CLASS ROOM : OB LH 204

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	1.15 PM 2.10 PM	2.10 PM 3.05 PM
MON	HDL (18EC56)	EMW (18EC55)	T E A B R E A K	DSP (18EC52)	ITC (18EC54)	L U N C H B R E A K	← DSP LAB(18ECL57)-A2/ HDL LAB(18ECL58)-A1 →		
TUE	EMW (18EC55)	DSP (18EC52)		PCS (18EC53)	TIM&E (18ES51)		ITC (18EC54)	HDL (18EC56)	EVS (18CIV59)
WED	TIM&E (18ES51)	HDL (18EC56)		PCS (18EC53)	DSP (18EC52)		← DSP LAB(18ECL57)-A3/ HDL LAB(18ECL58)-A2 →		
THU	DSP (18EC52)	ITC (18EC54)		EMW (18EC55)	TIM&E (18ES51)		HDL (18EC56)	PCS (18EC53)	T
FRI	PCS (18EC53)	TIM&E (18ES51)		ITC (18EC54)	EMW (18EC55)		← DSP LAB(18ECL57)-A1/ HDL LAB(18ECL58)-A3 →		

Sub-Code	Subject Name	Faculty Name
18ES51	Technological Innovation Management And Entrepreneurship	Mrs. Vishalini Divakar
18EC52	Digital Signal Processing	Mrs.Sangeetha.V
18EC53	Principles of Communication Systems	Dr. Rekha N
18EC54	Information Theory & Coding	Mrs. Bhargavi Ananth
18EC55	Electromagnetic Waves	Mrs. Bhanumathi A
18EC56	Verilog HDL	Dr. B.Sudharshan
18ECL57	Digital Signal Processing Laboratory	Mrs. Bhanumathi A, Mrs. Kavya B M
18ECL58	HDL Laboratory	Dr. B.Sudharshan, Dr. Rekha N- A1,A2 , Mrs. Bhargavi Ananth - A3
18CIV59	Environmental Studies	Mrs. Shylaja K R


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V SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. :10/10/2022

SEC : 'B'

CLASS TEACHER : Mrs. Bhargavi Ananth

CLASS ROOM : OB LH 205

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	DSP (18EC52)	PCS (18EC53)	T E A	HDL (18EC56)	EMW (18EC55)	L U N C H B R E A K	TIM&E (18ES51)	ITC (18EC54)	EVS (18CIV59)
TUE	PCS (18EC53)	TIM&E (18ES51)		ITC (18EC54)	EMW (18EC55)		← DSP LAB(18ECL57)-B2/ HDL LAB(18ECL58)-B1 →		
WED	← DSP LAB(18ECL57)-B3/ HDL LAB(18ECL58)-B2 →			HDL (18EC56)	ITC (18EC54)		DSP (18EC52)	TIM&E (18ES51)	
THU	EMW (18EC55)	HDL (18EC56)	B R E A K	DSP (18EC52)	PCS (18EC53)		← DSP LAB(18ECL57)-B1/ HDL LAB(18ECL58)-B3 →		
FRI	ITC (18EC54)	EMW (18EC55)		HDL (18EC56)	TIM&E (18ES51)		DSP (18EC52)	PCS (18EC53)	T

Sub-Code	Subject Name	Faculty Name
18ES51	Technological Innovation Management And Entrepreneurship	Mrs. Vishalini Divakar
18EC52	Digital Signal Processing	Mrs.Sangeetha.V
18EC53	Principles of Communication Systems	Dr. Rekha N
18EC54	Information Theory & Coding	Mrs. Bhargavi Ananth
18EC55	Electromagnetic Waves	Mrs. Kavya B M
18EC56	Verilog HDL	Dr. B.Sudharshan
18ECL57	Digital Signal Processing Laboratory	Mrs.Sangeetha.V, Mrs. Bhanumathi A
18ECL58	HDL Laboratory	Dr. Rekha N -B1 , Mrs. Bhargavi Ananth- B2, B3, Mrs. Kavya B M
18CIV59	Environmental Studies	Mrs. Shylaja K R

V. Sathya
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P. Suresh
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K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VII SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 19/9/2022

SEC : 'A'

CLASS TEACHER : Mrs. Pooja S

CLASS ROOM : OB LH 310

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	CRYPTO (18EC744)	CN (18EC71)	T E A B R E	E&E (18ME751)	VLSI (18EC72)	L U N C H B R E A K	← CN LAB (18ECL76) -A2 / VLSI LAB (18ECL77) -A1 →		
TUE	CRYPTO (18EC744)	CN (18EC71)		SC (18EC732)	VLSI (18EC72)		← Project Work Phase - 1 (18ECP78) →		
WED	SC (18EC732)	VLSI (18EC72)		CRYPTO (18EC744)	E&E (18ME751)		← CN LAB (18ECL76) -A3 / VLSI LAB (18ECL77) -A2 →		
THU	← CN LAB (18ECL76) -A1 / VLSI LAB (18ECL77) -A3 →			SC (18EC732)			E&E (18ME751)	CN (18EC71)	
FRI	CRYPTO (18EC744)	E&E (18ME751)	A K	CN (18EC71)	SC (18EC732)		VLSI (18EC72)	CRYPTO (18EC744) (T)	

Sub-Code	Subject Name	Faculty Name
18EC71	Computer Networks	Dr. Dinesh Kumar D S
18EC72	VLSI Design	Mr. Praveen.A.
18EC732	Satellite Communication Professional Elective - 2	Mrs. Pooja S
18EC744	Cryptography Professional Elective - 3	Dr. P.N Sudha
18ME751	Energy and Environment Open Elective -B	Dr. B Surekha
18ECL76	Computer Networks Lab	Mr. Saleem S Tevaramani, Dr. Dinesh Kumar D S
18ECL77	VLSI Laboratory	Mr. Praveen.A, Mr. Aswini Kumar G
18ECP78	Project Work Phase - 1	Dr. B.Sudharshan, Dr. Rekha N
	Internship	Mr. Santhosh Kumar B R

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VII SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 19/9/2022

SEC : 'B'

CLASS TEACHER : Mr. Aswini Kumar G

CLASS ROOM : OBLH 311

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7	
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM	4.00 PM
MON	E&E (18ME751)	CRYPTO (18EC744)	T E A B R E A K	VLSI (18EC72)	SC (18EC732)	L U N C H B R E A K	← Project Work Phase - 1 (18ECP78) →			
TUE	E&E (18ME751)	CRYPTO (18EC744)		VLSI (18EC72)	CN (18EC71)		← CN LAB (18ECL76) -B2 / VLSI LAB (18ECL77) -B1 →			
WED	CN (18EC71)	CRYPTO (18EC744)		SC (18EC732)	VLSI (18EC72)		E&E (18ME751)	CRYPTO (18EC744)-(T)		
THU	CRYPTO (18EC744)	SC (18EC732)		E&E (18ME751)	CN (18EC71)		← CN LAB (18ECL76) -B3 / VLSI LAB (18ECL77) -B2 →			
FRI	← CN LAB (18ECL76) -B1 / VLSI LAB (18ECL77) -B3 →			VLSI (18EC72)	CN (18EC71)		SC (18EC732)			

Sub-Code	Subject Name	Faculty Name
18EC71	Computer Networks	Dr. Dinesh Kumar D S
18EC72	VLSI Design	Mr. Aswini Kumar G
18EC732	Satellite Communication Professional Elective - 2	Mrs. Pooja S
18EC744	Cryptography Professional Elective - 3	Dr. P.N Sudha
18ME751	Energy and Environment Open Elective -B	Dr. B Surekha
18ECL76	Computer Networks Lab	Mr. Saleem S Tevaramani, Mr. Praveen.A.
18ECL77	VLSI Laboratory	Mrs. Pooja .S , Mr. Aswini Kumar G
18ECP78	Project Work Phase - 1	Dr. B.Sudharshan, Dr. Rekha N
	Internship	Mr. Santhosh Kumar B R

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VII SEMESTER TIME TABLE FOR THE YEAR 2022 (ODD SEMESTER)

W.E.F. : 19/9/2022
 SEC : 'B'

CLASS TEACHER : Mr. Saleem S Tevaramani
 CLASS ROOM : OB LH 409

PERIOD	1	2	10.20 AM 10.35 AM	3	4	12.25 PM 1.15 PM	5	6	7
TIME DAY	8.30 AM 9.25 AM	9.25 AM 10.20 AM		10.35 AM 11.30 AM	11.30 AM 12.25 PM		1.15 PM 2.10 PM	2.10 PM 3.05 PM	3.05 PM 4.00 PM
MON	E&E (18ME751)	CRYPTO (18EC744)	T E A B R E A K	OC (18TE71)	SC (18EC732)	L U N C H B R E A K			
TUE	E&E (18ME751)	CRYPTO (18EC744)		WC (18TE72)	OC (18TE71)		← WC LAB (18TEL76) →		
WED	OC (18TE71)	CRYPTO (18EC744)		SC (18EC732)	WC (18TE72)		E&E (18ME751)	CRYPTO (18EC744) -(T)	
THU	CRYPTO (18EC744)	SC (18EC732)		E&E (18ME751)	WC (18TE72)		← MWA LAB (18TEL77) →		
FRI	← Project Work Phase - 1 (18TEP78) →			WC (18TE72)	OC (18TE71)		SC (18EC732)		

Sub-Code	Subject Name	Faculty Name
18TE71	Optical Communication	Mr. Saleem S Tevaramani
18TE72	Wireless Communication	Dr. Sangappa S B
18EC732	Satellite Communication (Professional Elective - 2)	Mrs. Pooja S
18EC744	Cryptography (Professional Elective - 3)	Dr. P.N Sudha
18ME751	Energy and Environment Open Elective -B	Dr. B Surekha
18TEL76	Wireless Communication Laboratory	Dr. Dinesh Kumar D S
18TEL77	Microwave & Antennas Laboratory	Dr. P.N Sudha
18TEP78	Project Work Phase - 1	Dr. B.Sudharshan, Dr. Rekha N
Internship	Internship	Mr. Santhosh Kumar B R

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