






K.S. INSTITUTE OF TECHNOLOGY, BANGALORE -109
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
FDP JULY 2015- JUNE 2016

SL. No.	Name of the Event	Date	Association Details/ Coordinator	Resource person	Sem and Year	No Of Participants	Objectives of Event	PO's Mapped to	Photo
1	Five Days FDP on VLSI Design using Cadence Tools	29th June 2015 to 3rd July 2015	Dr. M.J. Shanthi Prasad,Mrs.Supriya V.G,Mrs.Anitha Senthilkumar,Mrs Prathima B.A,Mrs Sahana Salagare,Mrs.Laxmi H.R	Dr.Jayagouri,BMS college,B'lore	Faculties of KSIT and other	58	To enrich the knowledge of VLSI Design using cadence tools to research scholars, faculty members and post graduate students	PO6,PO7,PO9,PO10 ,PO11, PO12	
2	One day FDP on Writing Research Proposal	1st Feb 2016	Dr. Surekha	Dr.Nagaraj Prof,JNTU Hyderabad	KSIT Faculty	69	To give good insights into the synopsis preparation starting from Title selection, keywords, literature survey, objectives, methodology references as well timeline.	PO5,PO11,PO12	
3	FDP on Research Funds & IPR writing research proposal	27th,28th & 29th March 2016	KSIT B'lore	Dr.Cyril Prasan M.S.Engineering College,B'lore,Dr.K.N. Shridhara,Principal KSSEM,B'lore.Dr Shrinivas M.Jamakhandi,Director IISC-MSME.Prof.V Ramaprasad,IT consulatant ,Bengaluru	KSIT Faculty	23	To give good insights into the synopsis preparation starting from Title selection, keywords, literature survey, objectives, methodology references as well timeline.	PO5,PO6,PO9,PO10 PO11,PO12	

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