

NAME OF THE LAB: ANALOG CIRCUIT LAB-18ECL48

LOCATION: 406-OB IV Floor

DESCRIPTION: This lab has a **total area of 1014.75 sq ft.** It is utilized by 4th semester students for understanding and configuration and connectivity of BJT and FET Amplifier and design circuit using op-amps. They are also using lab for simulation of electronic circuits during the even semester of the academic calendar. During Odd semester the lab is used by students for final year projects and mini projects.

Safety: First Aid Kit available. All Electrical wires are protected by Miniature Circuit Breaker and fuses.

Hardware Equipments: CRO – 16, Power Supply-19, Function Generators-19, Multi Meters-7, Synthesized signal Generator-1

FACULTY IN-CHARGE: Mr. Satish Kumar B

NON- TEACHNING STAFF IN-CHARGE: Mr. Om Prakash