

#14, Raghuvanahalli, Kanakapura Road, Bangalore-560109.

Faculty Name	Praveen A
Designation	Assistant Professor
Educational Qualification	M.Tech
Experience in Years	Teaching: 9 Yrs. 4 Months.
	Industry: Nil
Areas of Interest	VLSI Design
Aadhar Number	4635 0416 3485
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Educational Details		
Examination/	College / University	Year of
Degree	Conege / University	Passing
UG	Visvesvaraya Technological University	2008
PG	Amrita Vishwa Vidyapeetham University	2010
PhD	Jain University	Pursuing

Publications

Journal Publication:

- 1. "Spectrum Estimation, Spectral Sensing Using Wavelet Packet Transform for Cognitive Radios" International Journal of Applied Research in Engineering and Science (IJARES-2014), Volume 01, (01) 2014, Pages 33-39 March 2014.
- **2. "Smart Guide at Museum"** Journal of Emerging Technologies and Innovative Research (JETIR-2019), Volume 06 Issue 4, Pages 416-418 April 2019.

Conference Papers:

- **1. "FPGA Implementation of Efficient Multiplier using Reversible Logic based Circuits"** National Conference on Innovative Technologies in Electrical and Electronics Engineering (NCITEE-16).
- 2. "A Multi-Point UART Controller Using Asynchronous FIFO Design with Gray Code Pointer" International Conference on Convergence of Science, Engineering and Management in Education and Research (ICCSEM 2013).

Awards

NIL

Professional Membership

NIL

Contact Details

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