

# K.S.INSTITUTE OF TECHNOLOGY, BANGALORE

(AFFILIATED TO VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM)

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.

### Engineering Statistics and Linear Algebra IV (Common to all Branches)

**Course Title:** COMPLEX ANALYSIS, PROBABILITY&STATISTICAL METHODS

**Code :** 18MAT41

**Credits:**03

**L-T-P :2-2-0**

**Contact Hours/Week:** 04

**Total Hours:**50

**Exam. Marks:**100

**IA Marks :30**

**Exam. Hours :** 03

**Course Learning Objectives:** This course will enable students to:

- Understand and Analyze Single and Multiple Random Variables, and their extension to Random Processes.
- Familiarization with the concept of Vector spaces and orthogonality with a qualitative insight into applications in communications.
- Compute the quantitative parameters for functions of single and Multiple Random Variables and Processes.
- Compute the quantitative parameters for Matrices and Linear Transformations.

#### Module-1

**Calculus of complex functions:** Review of function of a complex variable, limits, continuity, and differentiability. Analytic functions: Cauchy-Riemann equations in Cartesian and polar forms and consequences.

**Construction of analytic functions:** Milne-Thomson method-Problems.

#### Module-2

**Conformal transformations:** Introduction. Discussion of transformations:  $w = Z^2$ ,  $w = e^z$ ,  $w = z + 1$ ,  $(z \neq 0)$ . Bilinear transformations- Problems.

**Complex integration:** Line integral of a complex function-Cauchy's theorem and Cauchy's integral formula and problems.

#### Module-3

**Probability Distributions:** Review of basic probability theory. Random variables (discrete and continuous), probability mass/density functions. Binomial, Poisson, exponential and normal distributions- problems (No derivation for mean and standard deviation)-Illustrative examples.

#### Module-4

**Statistical Methods:** Correlation and regression-Karl Pearson's coefficient of correlation and rank correlation -problems. Regression analysis- lines of regression -problems.

**Curve Fitting:** Curve fitting by the method of least squares- fitting the curves of the form-  
 $y = ax + b$ ,  $y = ax^b$  and  $y = ax^2 + bx + c$ .

#### Module-5

**Joint probability distribution:** Joint Probability distribution for two discrete random variables, expectation and covariance.

**Sampling Theory:** Introduction to sampling distributions, standard error, Type-I and Type-II errors. Test of hypothesis for means, student's t-distribution, Chi-square distribution as a test of goodness of fit.

**Course Outcomes:**

At the end of the course the student will be able to:

- Use the concepts of analytic function and complex potentials to solve the problems arising in electromagnetic field theory.
- Utilize conformal transformation and complex integral arising in aerofoil theory, fluid flow visualization and image processing.
- Apply discrete and continuous probability distributions in analyzing the probability models arising in engineering field.
- Make use of the correlation and regression analysis to fit a suitable mathematical model for the statistical data.
- Construct joint probability distributions and demonstrate the validity of testing the hypothesis.

**Question paper pattern:**

5. The question paper will have ten full questions carrying equal marks.
  6. Each full question will be for 20 marks.
- There will be two full questions (with a maximum of four sub- questions) from each module.

Sl. No.	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
<b>Textbooks</b>				
1	Advanced Engineering Mathematics	E. Kreyszig	John Wiley & Sons	10 <sup>th</sup> Edition,2016
2	Higher Engineering Mathematics	B. S. Grewal	Khanna Publishers	44 <sup>th</sup> Edition, 2017
3	Engineering Mathematics	Srimanta Pal et al	Oxford University Press	3 <sup>rd</sup> Edition,2016
<b>Reference Books</b>				
1	Advanced Engineering Mathematics	C. Ray Wylie, Louis C.Barrett	McGraw-Hill	6 <sup>th</sup> Edition 1995
2	Introductory Methods of Numerical Analysis	S.S.Sastry	Prentice Hall of India	4 <sup>th</sup> Edition 2010
3	Higher Engineering Mathematics	B. V. Ramana	McGraw-Hill	11 <sup>th</sup> Edition,2010
4	A Text Book of Engineering Mathematics	N. P. Bali and Manish Goyal	Laxmi Publications	2014

**Web links and Video Lectures:**

1. <http://nptel.ac.in/courses.php?disciplineID=111>
2. [http://www.class-central.com/subject/math\(MOOCs\)](http://www.class-central.com/subject/math(MOOCs))
3. <http://academicearth.org/>
4. VTU EDUSAT PROGRAMME - 20

B. E. (EC / TC) Choice Based Credit System (CBCS) and Outcome Based Education (OBE) SEMESTER – IV			
ANALOG CIRCUITS			
Subject Code	18EC42	CIE Marks	40
Number of Lecture Hours/Week	3+2 (Tutorial)	SEE Marks	60
		Exam Hours	03
CREDITS – 04			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Explain various BJT parameters, connections and configurations.</li> <li>• Design and demonstrate the diode circuits and transistor amplifiers.</li> <li>• Explain various types of FET biasing, and demonstrate the use of FET amplifiers.</li> <li>• Construct frequency response of FET amplifiers at various frequencies.</li> <li>• Analyze Power amplifier circuits in different modes of operation.</li> <li>• Construct Feedback and Oscillator circuits using FET.</li> </ul>			
Modules			RBT Level
Module -1			
<p><b>BJT Biasing: Biasing in BJT amplifier circuits:</b> The Classical Discrete circuit bias (Voltage-divider bias), Biasing using a collector to base feedback resistor.</p> <p><b>Small signal operation and Models:</b> Collector current and transconductance, Base current and input resistance, Emitter current and input resistance, voltage gain, Separating the signal and the DC quantities, The hybrid <math>\Pi</math> model.</p> <p><b>MOSFETs: Biasing in MOS amplifier circuits:</b> Fixing <math>V_{GS}</math>, Fixing <math>V_G</math>, Drain to Gate feedback resistor.</p> <p><b>Small signal operation and modeling:</b> The DC bias point, signal current in drain, voltage gain, small signal equivalent circuit models, transconductance.</p> <p>[Text 1: 3.5(3.5.1, 3.5.3), 3.6(3.6.1 to 3.6.6), 4.5(4.5.1, 4.5.2, 4.5.3), 4.6(4.6.1 to 4.6.6) ]</p>			L1, L2, L3
Module -2			
<p><b>MOSFET Amplifier configuration:</b> Basic configurations, characterizing amplifiers, CS amplifier with and without source resistance <math>R_S</math>, Source follower.</p> <p><b>MOSFET internal capacitances and High frequency model:</b> The gate capacitive effect, Junction capacitances, High frequency model.</p> <p><b>Frequency response of the CS amplifier:</b> The three frequency bands, high frequency response, Low frequency response.</p> <p><b>Oscillators:</b> FET based Phase shift oscillator, LC and Crystal Oscillators (no derivation)</p> <p>[Text 1: 4.7(4.7.1 to 4.7.4, 4.7.6) 4.8(4.8.1, 4.8.2, 4.8.3), 4.9, 12.2.2, 12.3.1, 12.3.2]</p>			L1, L2, L3
Module -3			
<p><b>Feedback Amplifier:</b> General feedback structure, Properties of negative feedback, The Four Basic Feedback Topologies, The series-shunt, series-series, shunt-shunt and shunt-series amplifiers (Qualitative Analysis).</p> <p><b>Output Stages and Power Amplifiers:</b> Introduction, Classification of output stages, Class A output stage, Class B output stage: Transfer Characteristics, Power Dissipation, Power Conversion efficiency, Class AB output stage, Class C tuned Amplifier.</p> <p>[Text 1: 7.1, 7.2, 7.3, 7.4.1, 7.5.1, 7.6 (7.6.1 to 7.6.3), 13.1, 13.2, 13.3(13.3.1, 13.3.2, 13.3.3, 13.4, 13.7)]</p>			L1, L2, L3
Module -4			
<p><b>Op-Amp with Negative Feedback and general applications</b></p> <p>Inverting and Non inverting Amplifiers – Closed Loop voltage gain, Input impedance, Output impedance, Bandwidth with feedback. DC and AC Amplifiers, Summing, Scaling and Averaging Amplifiers, Instrumentation amplifier, Comparators, Zero Crossing Detector, Schmitt trigger.</p> <p>[Text 2: 3.3(3.3.1 to 3.3.6), 3.4(3.4.1 to 3.4.5) 6.2, 6.5, 6.6 (6.6.1), 8.2, 8.3, 8.4]</p>			L1, L2, L3
Module -5			

<p><b>Op-Amp Circuits:</b> DAC - Weighted resistor and R-2R ladder, ADC- Successive approximation type, Small Signal half wave rectifier, Active Filters, First and second order low-pass and high-pass Butterworth filters, Band-pass filters, Band reject filters.</p> <p><b>555 Timer and its applications:</b> Monostable and a stable Multivibrators.</p> <p>[Text 2: 8.11(8.11.1a, 8.11.1b), 8.11.2a, 8.12.2, 7.2, 7.3, 7.4, 7.5, 7.6, 7.8, 7.9, 9.4.1, 9.4.1(a), 9.4.3, 9.4.3(a)]</p>	<p><b>L1, L2, L3</b></p>
<p><b>Course Outcomes:</b> At the end of this course students will demonstrate the ability to</p> <ul style="list-style-type: none"> <li>• Identify the performance characteristics and parameters of BJT and FET amplifier using small signal model.</li> <li>• Design and Analyze the MOSFET amplifier and Oscillator circuits.</li> <li>• Design and Analyze the BJT power amplifier.</li> <li>• Identify the functioning and application of linear ICs.</li> <li>• Design of Linear IC based circuits Like DAC, ADC, Rectifier and Filters.</li> </ul>	
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>	
<p><b>Text Books:</b></p> <ol style="list-style-type: none"> <li>1. Microelectronic Circuits, Theory and Applications, Adel S Sedra, Kenneth C Smith, 6<sup>th</sup> Edition, Oxford, 2015. ISBN: 978-0-19-808913-1</li> <li>2. Op-Amps and Linear Integrated Circuits, Ramakant A Gayakwad, 4<sup>th</sup> Edition. Pearson Education, 2000. ISBN: 8120320581</li> </ol>	
<p><b>Reference Books:</b></p> <ol style="list-style-type: none"> <li>1. Electronic Devices and Circuit Theory, Robert L Boylestad and Louis Nashelsky, 11<sup>th</sup> Edition, Pearson Education, 2013, ISBN: 978-93-325-4260-0.</li> <li>2. Fundamentals of Microelectronics, Behzad Razavi, 2<sup>nd</sup> Edition, John Wiley, 2015, ISBN 978-81-265-7135-2</li> <li>3. J. Millman &amp; C. C. Halkias—Integrated Electronics, 2<sup>nd</sup> edition, 2010, TMH. ISBN 0-07-462245-5</li> </ol>	

**Web Link and Video Lectures:**

1. <https://nptel.ac.in/courses/117/101/117101106/>
2. <https://www.classcentral.com/course/swyam-analog-electronic-circuit-13894>

B. E. (EC / TC) ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER –III CONTROL SYSTEMS			
<b>Course Code</b>	<b>18EC43</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (08 Hours per Module)</b>	<b>Exam Hours</b>	<b>03</b>
CREDITS – 03			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Understand the basic features, configurations and application of controlsystems.</li> <li>• Understand various terminologies and definitions for the controlsystems.</li> <li>• Learn how to find a mathematical model of electrical, mechanical and electro- mechanical systems.</li> <li>• Know how to find time response from the transferfunction.</li> <li>• Find the transfer function via Mason s’rule.</li> <li>• Analyze the stability of a system from the transferfunction.</li> </ul>			
Modules			RBT Level
Module – 1			
<b>Introduction to Control Systems:</b> Types of Control Systems, Effect of Feedback Systems, Differential equation of Physical Systems –Mechanical Systems, Electrical Systems, Electromechanical systems, Analogous Systems.			<b>L1, L2, L3</b>
Module – 2			
<b>Block diagrams and signal flow graphs:</b> Transfer functions, Block diagram algebra and Signal Flow graphs.			<b>L1, L2, L3</b>
Module – 3			
<b>Time Response of feedback control systems:</b> Standard test signals, Unit step response of First and Second order Systems. Time response specifications, Time response specifications of second order systems, steady state errors and error constants. Introduction to PI, PD and PID Controllers (excluding design).			<b>L1, L2, L3</b>
Module – 4			
<b>Stability analysis:</b> Concepts of stability, Necessary conditions for Stability, Routhstability criterion, Relative stability analysis: more on the Routh stability criterion. Introduction to Root-Locus Techniques, The root locus concepts, Construction of rootloci.			<b>L1, L2, L3</b>
<b>Frequency domain analysis and stability:</b> Correlation between time and frequency response, Bode Plots, Experimental determination of transfer function.			
Module – 5			
Introduction to Polar Plots, (Inverse Polar Plotsexcluded) Mathematical preliminaries, Nyquist Stability criterion, (System s with transportation lag excluded) Introduction to lead, lag and lead- lag compensating networks (excluding design).			<b>L1, L2, L3</b>
<b>Introduction to State variable analysis:</b> Concepts of state, state variable and state models for electrical systems, Solution of state equations.			

**Course Outcomes:** At the end of the course, the students will be able to

- Develop the mathematical model of mechanical / electrical systems and obtain its transfer function using block reduction method /Signal flow graph method
- Ability to relate transient performance parameters (overshoot, rise time, peak time and settling time) for the given system and to evaluate steady state error.
- Identify various stability criteria and Determine the stability of a system in the time domain using Routh-Hurwitz criterion and Root-locus technique.
- Determine the stability of a system in the frequency domain using Nyquist and bode plots
- Develop a control system model in continuous and discrete time using state variable techniques

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Book:**

J. Nagarath and M.Gopal, "Control Systems Engineering", New Age International(P) Limited, Publishers, Fifth edition- 2005, ISBN: 81 - 224 - 2008-7.

**Reference Books:**

1. "Modern Control Engineering," K.Ogata, Pearson Education Asia/ PHI, 4<sup>th</sup> edition, 2002.  
ISBN 978 - 81 - 203 - 4010 - 7.
2. "Automatic Control Systems", Benjamin C. Kuo, John Wiley India Pvt. Ltd., 8<sup>th</sup> edition, 2008.
3. "Feedback and Control System," Joseph J Distefano III et al., Schaum's Outlines, TMH, 2<sup>nd</sup> Edition 2007.

**Web Link and Video Lectures:**

1. <https://www.edx.org/course/introduction-to-control-system-design-a-first-look>
2. <https://www.classcentral.com/course/swayam-control-systems-13963>
3. [https://swayam.gov.in/nd1\\_noc19\\_de04/preview](https://swayam.gov.in/nd1_noc19_de04/preview)

<b>B. E. (EC / TC)</b> <b>Choice Based Credit System (CBCS) and Outcome Based Education (OBE)</b> <b>SEMESTER – IV</b>			
<b>ENGINEERING STATISTICS and LINEAR ALGEBRA</b>			
<b>Course Code</b>	<b>18EC44</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours per Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Understand and Analyze Single and Multiple Random Variables, and their extension to Random Processes.</li> <li>• Familiarization with the concept of Vector spaces and orthogonality with a qualitative insight into applications in communications.</li> <li>• Compute the quantitative parameters for functions of single and Multiple Random Variables and Processes.</li> <li>• Compute the quantitative parameters for Matrices and Linear Transformations.</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
<b>Single Random Variables:</b> Definition of random variables, cumulative distribution function continuous and discrete random variables; probability mass function, probability density functions and properties; Expectations, Characteristic functions, Functions of single Random Variables, Conditioned Random variables. Application exercises to Some special distributions: Uniform, Exponential, Laplace, Gaussian; Binomial, and Poisson distribution. <b>(Chapter 4 Text 1)</b>			<b>L1, L2, L3</b>
<b>Module -2</b>			
<b>Multiple Random variables:</b> Concept, Two variable CDF and PDF, Two Variable expectations (Correlation, orthogonality, Independent), Two variable transformation, Two Gaussian Random variables, Sum of two independent Random Variables, Sum of IID Random Variables – Central limit Theorem and law of large numbers, Conditional joint Probabilities, Application exercises to Chi-square RV, Student-TRV, Cauchy and Rayleigh RVs. <b>(Chapter 5 Text1)</b>			<b>L1, L2, L3</b>
<b>Module-3</b>			
<b>Random Processes:</b> Ensemble, PDF, Independence, Expectations, Stationarity, Correlation Functions (ACF, CCF, Addition, and Multiplication), Ergodic Random Processes, Power Spectral Densities (Wiener Khinchin, Addition and Multiplication of RPs, Cross spectral densities), Linear Systems (output Mean, Cross correlation and Auto correlation of Input and output), Exercises with Noise. <b>(Chapter 6 Text1)</b>			<b>L1, L2, L3</b>
<b>Module -4</b>			
<b>Vector Spaces:</b> Vector spaces and Null subspaces, Rank and Row reduced form, Independence, Basis and dimension, Dimensions of the four subspaces, Rank-Nullity Theorem, Linear Transformations <b>Orthogonality:</b> Orthogonal Vectors and Subspaces, Projections and Least squares, Orthogonal Bases and Gram-Schmidt Orthogonalization procedure. <b>(Refer Chapters 2 and 3 Text2)</b>			<b>L1, L2, L3</b>
<b>Module -5</b>			
<b>Determinants:</b> Properties of Determinants, Permutations and Cofactors. <b>(Refer Chapter 4, Text 2)</b> <b>Eigenvalues and Eigen vectors:</b> Review of Eigenvalues and Diagonalization of a Matrix, Special Matrices (Positive Definite, Symmetric) and their properties, Singular Value Decomposition. <b>(Refer Chapter 5, Text 2)</b>			<b>L1, L2, L3</b>

**Course Outcomes:** After studying this course, students will be able to:

- Identify Random Variables to extract quantitative statistical parameters and apply the same for special distributions.
- Analyze statistical representations and Eigen values of some special matrices and demonstrate the same using MATLAB.
- Analyze Random events in typical communication events to extract quantitative statistical parameters.
- Analyze vectors and vector spaces using suitable transformations and basis function sets.
- Analyze the concept of Multiple Random variables to extract quantitative statistical parameters.

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Books:**

1. Richard H Williams, "Probability, Statistics and Random Processes for Engineers" Cengage Learning, 1st Edition, 2003, ISBN 13: 978-0-534- 36888-3, ISBN 10:0-534-36888-3.
2. Gilbert Strang, "Linear Algebra and its Applications", Cengage Learning, 4th Edition, 2006, ISBN 97809802327

**Reference Books:**

1. Hwei P. Hsu, "Theory and Problems of Probability, Random Variables, and Random Processes" Schaums Outline Series, McGraw Hill. ISBN 10: 0-07-030644-3.
2. K. N. Hari Bhat, K Anitha Sheela, Jayant Ganguly, "Probability Theory and Stochastic Processes for Engineers", Cengage Learning India, 2019, ISBN: Not in book

**Web Link and Video Lectures:**

1. [www.coursera.org/courses?query=linear%20algebra](http://www.coursera.org/courses?query=linear%20algebra)
2. [www.classcentral.com/course/matrix-algebra-engineers-11986](http://www.classcentral.com/course/matrix-algebra-engineers-11986)

**B. E. (EC / TC)**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – IV**

**SIGNALS AND SYSTEMS**

<b>Course Code</b>	<b>18EC45</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours per Module)</b>	<b>Exam Hours</b>	<b>03</b>

**CREDITS – 03**

**Course Learning Objectives:** This course will enable students to:

- Understand the mathematical description of continuous and discrete time signals and systems.
- Analyze the signals in time domain using convolution sum and Integral.
- Classify signals into different categories based on their properties.
- Analyze Linear Time Invariant (LTI) systems in time and transform domains.

<b>Module-1</b>	<b>RBT Level</b>
<p><b>Introduction and Classification of signals:</b> Definition of signal and systems, communication and control system as examples Classification of signals.</p> <p><b>Basic Operations on signals:</b> Amplitude scaling, addition, multiplication, differentiation, integration, time scaling, time shift and time reversal.</p> <p><b>Elementary signals/Functions:</b> Exponential, sinusoidal, step, impulse and ramp functions. Expression of triangular, rectangular and other waveforms in terms of elementary signals.</p>	<b>L1, L2, L3</b>
<b>Module -2</b>	
<p><b>System Classification and properties:</b> Linear-nonlinear, Time variant-invariant, causal-noncausal, static-dynamic, stable-unstable, invertible.</p> <p><b>Time domain representation of LTI System:</b> Impulse response, convolution sum, convolution integral. Computation of convolution sum and convolution integral using graphical method for unit step and unit step, unit step and exponential, exponential and exponential, unit step and rectangular, and rectangular and rectangular.</p>	<b>L1, L2, L3</b>
<b>Module-3</b>	
<p><b>LTI system Properties in terms of impulse response:</b> System interconnection, Memory less, Causal, Stable, Invertible and Deconvolution, and step response.</p> <p><b>Fourier Representation of Periodic Signals:</b> CTF S properties and basic problems.</p>	<b>L1, L2, L3</b>
<b>Module -4</b>	
<p><b>Fourier Representation of aperiodic Signals:</b> Introduction to Fourier Transform &amp; DTFT, Definition and basic problems.</p> <p><b>Properties of Fourier Transform:</b> Linearity, Time shift, Frequency shift, Scaling, Differentiation and Integration, Convolution and Modulation, Parseval's theorem and problems on properties of Fourier Transform.</p>	<b>L1, L2, L3</b>
<b>Module -5</b>	
<p><b>The Z-Transforms:</b> Z transform, properties of the region of convergence, properties of the Z-transform, Inverse Z-transform, Causality and stability, Transform analysis of LTI systems.</p>	<b>L1, L2, L3</b>
<p><b>Course Outcomes:</b> At the end of the course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Apply the basic operations on signals and classify elementary signals.</li> <li>• Classify the various systems and analyze the concepts of convolution sum &amp; integral on signals and</li> <li>• Examine the system properties and represent periodic continuous/discrete signals in time and frequency domain using Fourier series.</li> <li>• Make use of the properties of Fourier Transform on aperiodic signals to represent the signals in frequency domain.</li> <li>• Make use of Z-transforms, inverse Z-transforms and transfer functions to analyze the complex LTI systems.</li> </ul>	

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Book:**

**Simon Haykins and Barry Van Veen**, "Signals and Systems", 2nd Edition, 2008, Wiley India. ISBN 9971-51-239-4.

**Reference Books:**

1. **Michael Roberts**, "Fundamentals of Signals & Systems", 2nd edition, Tata McGraw-Hill, 2010, ISBN 978-0-07-070221-9.
2. **Alan V Oppenheim, Alan S, Willsky and A Hamid Nawab**, "Signals and Systems" Pearson Education Asia / PHI, 2nd edition, 1997. Indian Reprint 2002.
3. **H.P Hsu, R. Ranjan**, "Signals and Systems", Scham's outlines, TMH, 2006.
4. **B.P.Lathi**, "Linear Systems and Signals", Oxford University Press, 2005.
5. **Ganesh Rao and Satish Tunga**, "Signals and Systems", Pearson/Sanguine.

**Web Link and Video Lectures:**

1. [www.edx.org/course/signals-and-systems-part-1](http://www.edx.org/course/signals-and-systems-part-1)
2. [www.classcentral.com/course/swayam-principles-of-signals-and-systems-9900](http://www.classcentral.com/course/swayam-principles-of-signals-and-systems-9900)

**B. E. (EC / TC)**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – IV**

**MICROCONTROLLER**

<b>Course Code</b>	<b>18EC46</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours per Module)</b>	<b>Exam Hours</b>	<b>03</b>

**CREDITS – 03**

**Course Learning Objectives:** This course will enable students to:

- Understand the difference between a Microprocessor and a Microcontroller and embedded microcontrollers.
- Familiarize the basic architecture of 8051 microcontroller.
- Program 8051 microprocessor using Assembly Level Language and C.
- Understand the interrupt system of 8051 and the use of interrupts.
- Understand the operation and use of inbuilt Timers/Counters and Serial port of 8051.
- Interface 8051 to external memory and I/O devices using its I/O ports.

<b>Module-1</b>	<b>RBT Level</b>
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**8051 Microcontroller:** Microprocessor Vs Microcontroller, Embedded Systems, Embedded Microcontrollers, 8051 Architecture- Registers, Pin diagram, I/O ports functions, Internal Memory organization. External Memory (ROM & RAM) interfacing.

**L1, L2**

<b>Module -2</b>
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**8051 Instruction Set:** Addressing Modes, Data Transfer instructions, Arithmetic instructions, Logical instructions, Branch instructions, Bit manipulation instructions. Simple Assembly language program examples (without loops) to use these instructions.

**L1, L2**

<b>Module-3</b>
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**8051 Stack, I/O Port Interfacing and Programming:** 8051 Stack, Stack and Subroutine instructions. Assembly language program examples on subroutine and involving loops. Interfacing simple switch and LED to I/O ports to switch on/off LED with respect to switch status.

**L1, L2, L3**

<b>Module -4</b>
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**8051 Timers and Serial Port:** 8051 Timers and Counters – Operation and Assembly language programming to generate a pulse using Mode-1 and a square wave using Mode- 2 on a port pin. 8051 Serial Communication- Basics of Serial Data Communication, RS- 232 standard, 9 pin RS232 signals, Simple Serial Port programming in Assembly and C to transmit a message and to receive data serially.

**L1, L2, L3**

<b>Module -5</b>
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**8051 Interrupts and Interfacing Applications:** 8051 Interrupts. 8051 Assembly language programming to generate an external interrupt using a switch, 8051 C programming to generate a square waveform on a port pin using a Timer interrupt. Interfacing 8051 to ADC-0804, DAC, LCD and Stepper motor and their 8051 Assembly language interfacing programming.

**L1, L2, L3**

**Course outcomes:** At the end of the course, students will be able to:

- Distinguish the role of functional units in the architecture of 8051 microcontroller
- Identify various instructions of 8051 Microcontroller
- Build solutions using assembly level language and high level language
- Make use of timers/counters, serial port and interrupts to generate delay and perform serial communication
- Design interfacing of peripherals to 8051 Microcontroller

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Books:**

1. “The 8051 Microcontroller and Embedded Systems – using assembly and C”, Muhammad Ali Mazidi and Janice Gillespie Mazidi and Rollin D. McKinlay; PHI, 2006/Pearson, 2006.
2. “The 8051 Microcontroller”, Kenneth J. Ayala, 3rd Edition, Thomson/Cengage Learning.

**Reference Books:**

1. “The 8051 Microcontroller Based Embedded Systems”, Manish K Patel, McGraw Hill, 2014, ISBN: 978-93-329-0125-4.
2. “Microcontrollers: Architecture, Programming, Interfacing and System Design”, Raj Kamal, Pearson Education, 2005.

**Web Link and Video Lectures:**

1. [www.edx.org/learn/microcontrollers](http://www.edx.org/learn/microcontrollers)
2. [www.nptel.ac.in/courses/108/105/108105102/](http://www.nptel.ac.in/courses/108/105/108105102/)

**B. E. (EC / TC)**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER –**  
**IV**

**MICROCONTROLLER LABORATORY**

<b>Laboratory Code</b>	<b>18ECL47</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number ofLectureHours/Week</b>	<b>02Hr Tutorial (Instructions) + 02 Hours Laboratory</b>	<b>SEE Marks</b>	<b>60</b>
<b>RBT Levels</b>	<b>L1, L2, L3</b>	<b>Exam Hours</b>	<b>03</b>

**CREDITS – 02**

**Course Learning Objectives:** This laboratory course enables students to

- Understand the basics of microcontroller and its applications.
- Have in-depth knowledge of 8051 assembly language programming.
- Understand controlling the devices using C programming.
- The concepts of I/O interfacing for developing real time embedded systems.

**Laboratory Experiments**

**I. PROGRAMMING**

1. Data Transfer: Block Move, Exchange, Sorting, Finding largest element in an array.
2. Arithmetic Instructions - Addition/subtraction, multiplication and division, square, Cube – (16 bits Arithmetic operations – bit addressable).
3. Counters.
4. Boolean & Logical Instructions (Bit manipulations).
5. Conditional CALL & RETURN.
6. Code conversion: BCD–ASCII; ASCII–Decimal; Decimal-ASCII; HEX-Decimal and Decimal- HEX.
7. Program to generate delay, Programs using serial port and on-chip timer/counter.

**II. INTERFACING**

1. Interface a simple toggle switch to 8051 and write an ALP to generate an interrupt which switches on an LED (i) continuously as long as switch is on and (ii) only once for a small time when the switch is turned on.
2. Write a C program to (i) transmit and (ii) to receive a set of characters serially by interfacing 8051 to a terminal.
3. Write ALPs to generate waveforms using ADC interface.
4. Write ALP to interface an LCD display and to display a message on it.
5. Write ALP to interface a Stepper Motor to 8051 to rotate the motor.
6. Write ALP to interface ADC-0804 and convert an analog input connected to it.

**Course Outcomes:** On the completion of this laboratory course, the students will be able to:

- Develop Assembly level program for transferring data and to perform arithmetic operations like addition, multiplication etc
- Develop Assembly level program to act as a counter using subroutine
- Make use of timers for generating the delay and serial communication ports for transferring the data serially
- Examine the use of interrupts in controlling the switches connected to the ports
- Test for the working of interface like ADC, stepper motor, LCD etc

**Conduct of Practical Examination:**

- All laboratory experiments are to be included for practical examination.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
- Change of experiment is allowed only once and 15% Marks allotted to the procedure part to be made zero.

<b>B. E. (EC / TC)</b> <b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – IV</b>			
<b>ANALOG CIRCUITS LABORATORY</b>			
<b>Laboratory Code</b>	<b>18ECL48</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>02Hr Tutorial (Instructions) + 02 Hours Laboratory</b>	<b>SEE Marks</b>	<b>60</b>
<b>RBT Level</b>	<b>L1, L2, L3</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 02</b>			
<b>Course Learning Objectives:</b> This laboratory course enables students to			
<ul style="list-style-type: none"> <li>• Understand the circuit configurations and connectivity of BJT and FET Amplifiers and Study of frequency response</li> <li>• Design and test of analog circuits using OPAMPs</li> <li>• Understand the feedback configurations of transistor and OPAMP circuits</li> <li>• Use of circuit simulation for the analysis of electronic circuits.</li> </ul>			
<b>Laboratory Experiments</b>			
<b>PART A : Hardware Experiments</b>			
1. Design and setup the Common Source JFET/MOSFET amplifier and plot the frequency response.			
2. Design and set up the BJT common emitter voltage amplifier with and without feedback and determine the gain- bandwidth product, input and output impedances.			
3. Design and set-up BJT/FET i) Colpitts Oscillator, and ii) Crystal Oscillator			
4. Design active second order Butterworth low pass and high pass filters.			
5. Design Adder, Integrator and Differentiator circuits using Op-Amp			
6. Test a comparator circuit and design a Schmitt trigger for the given UTP and LTP values and obtain the hysteresis.			
7. Design 4 bit R – 2R Op-Amp Digital to Analog Converter (i) using 4 bit binary input from toggle switches and (ii) by generating digital inputs using mod-16 counter.			
8. Design Monostable and a stable Multivibrator using 555 Timer.			
<b>PART-B : Simulation using EDA software</b> (EDWinXP, PSpice, MultiSim, Proteus, CircuitLab or any other equivalent tool can be used)			
1. RC Phase shift oscillator and Hartley oscillator			
2. Narrow Band-pass Filter and Narrow band-reject filter			
3. Precision Half and full wave rectifier			
4. Monostable and A stable Multivibrator using 555 Timer.			
<b>Course Outcomes:</b> On the completion of this laboratory course, the students will be able to:			
<ul style="list-style-type: none"> <li>• Design and test the setup of BJT and FET amplifiers and study its frequency response.</li> <li>• Design and test oscillators by calculating its frequency of oscillations.</li> <li>• Design and analyze the applications of Op-Amps for DACs, Filters, Schmitt Trigger, and adder, Integrator and differentiator circuits.</li> <li>• Analyze and test the Multivibrators using 555 Timer.</li> <li>• Analyze and implement the circuits of Oscillators, Filters, Rectifiers and Multivibrators using BJTs, ICs 741 and 555 through simulation software.</li> </ul>			
<b>Conduct of Practical Examination:</b>			
<ul style="list-style-type: none"> <li>• All laboratory experiments are to be included for practical examination.</li> <li>• Students are allowed to pick one experiment from the lot.</li> <li>• Strictly follow the instructions as printed on the cover page of answers script for breakup of marks.</li> <li>• Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.</li> </ul>			

**Reference Books:**

1. David A Bell, "Fundamentals of Electronic Devices and Circuits Lab Manual, 5<sup>th</sup> Edition, 2009, Oxford University Press.

**B. E. Common to all Programmes**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE)**  
**SEMESTER -IV**

**ADDITIONAL MATHEMATICS – II**

(Mandatory Learning Course: Common to All Programmes)

(A Bridge course for Lateral Entry students under Diploma quota to BE/B. Tech. programmes)

Course Code	<b>18MATDIP41</b>	CIE Marks	40
Teaching Hours/Week (L:T:P)	(2:1:0)	SEE Marks	60
Credits	<b>0</b>	Exam Hours	03

**Course Learning Objectives:**

- To provide essential concepts of linear algebra, second & higher order differential equations along with methods to solve them.
- To provide an insight into elementary probability theory and numerical methods.

**Module-1**

**Linear Algebra:** Introduction - rank of matrix by elementary row operations - Echelon form. Consistency of system of linear equations - Gauss elimination method. Eigen values and Eigen vectors of a square matrix. Problems.

**Module-2**

**Numerical Methods:** Finite differences. Interpolation/extrapolation using Newton's forward and backward difference formulae (Statements only)-problems. Solution of polynomial and transcendental equations – Newton-Raphson and Regula-Falsi methods (only formulae)- Illustrative examples. Numerical integration: Simpson's one third rule and Weddle's rule (without proof) Problems.

**Module-3**

**Higher order ODE's:** Linear differential equations of second and higher order equations with constant coefficients. Homogeneous /non-homogeneous equations. Inverse differential operators. [*Particular Integral restricted to  $R(x) = e^{ax}, \sin ax / \cos ax$  for  $f(D)y = R(x)$ .*]

**Module-4**

**Partial Differential Equations (PDE's):-** Formation of PDE's by elimination of arbitrary constants and functions. Solution of non-homogeneous PDE by direct integration. Homogeneous PDEs involving derivative with respect to one independent variable only.

**Module-5**

**Probability:** Introduction. Sample space and events. Axioms of probability. Addition & multiplication theorems. Conditional probability, Bayes's theorem, problems.

**Course Outcomes:** At the end of the course the student will be able to:

- CO1: Solve systems of linear equations using matrix algebra.
- CO2: Apply the knowledge of numerical methods in modelling and solving engineering problems.
- CO3: Make use of analytical methods to solve higher order differential equations.
- CO4: Classify partial differential equations and solve them by exact methods.
- CO5: Apply elementary probability theory and solve related problems.

**Question paper pattern:**

7. The question paper will have ten full questions carrying equal marks.
8. Each full question will be for 20 marks.
  - There will be two full questions (with a maximum of four sub-questions) from each module.
  - Each full question will have sub- question covering all the topics under a module.
  - The students will have to answer five full questions, selecting one full question from each

Sl No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
<b>Textbook</b>				
1	Higher Engineering Mathematics	B.S. Grewal	Khanna Publishers	43 <sup>rd</sup> Edition, 2015
<b>Reference Books</b>				

1	Advanced Engineering Mathematics	E. Kreyszig	John Wiley & Sons	10 <sup>th</sup> Edition, 2015
2	Engineering Mathematics	N. P. Bali and Manish Goyal	Laxmi Publishers	7th Edition, 2007
3	Engineering Mathematics Vol. I	Rohit Khurana	Cengage Learning	1 <sup>st</sup> Edition, 2015

# K.S.INSTITUTE OF TECHNOLOGY, BANGALORE

(AFFILIATED TO VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM)

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.  
DIGITAL COMMUNICATION VI**

**Course Title: DIGITAL COMMUNICATION**

**Credits:03**

**Contact Hours/Week: 04**

**Exam. Marks:100**

**Exam. Hours : 03**

**Course Code : 18EC61**

**L-T-P :4-0-0**

**Total Hours:50**

**IA Marks :30**

<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Understand the mathematical representation of signal, symbol, and noise.</li> <li>• Understand the concept of signal processing of digital data and signal conversion to symbols at the transmitter and receiver.</li> <li>• Compute performance metrics and parameters for symbol processing and recovery in ideal and corrupted channel conditions.</li> <li>• Compute performance parameters and mitigate channel induced impediments in corrupted channel conditions.</li> </ul>	
<b>Module-1</b>	<b>RBT Level</b>
<p><b>Bandpass Signal to Equivalent Low pass:</b> Hilbert Transform, Pre-envelopes, Complex envelopes, Canonical representation of bandpass signals, Complex low pass representation of bandpass systems, Complex representation of band pass signals and systems (<b>Text 1: 2.8, 2.9, 2.10, 2.11, 2.12, 2.13</b>).</p> <p><b>Line codes:</b> Unipolar, Polar, Bipolar (AMI) and Manchester code and their power spectral densities (<b>Text 1: Ch 6.10</b>).</p> <p>Overview of HDB3, B3ZS, B6ZS (<b>Ref. 1: 7.2</b>)</p>	<b>L1,L2,L3</b>
<b>Module-2</b>	
<p><b>Signaling over AWGN Channels-</b> Introduction, Geometric representation of signals, Gram-Schmidt Orthogonalization procedure, Conversion of the continuous AWGN channel into a vector channel, Optimum receiver using coherent detection: ML Decoding, Correlation receiver, matched filter receiver (<b>Text 1: 7.1, 7.2, 7.3, 7.4</b>).</p>	<b>L1,L2,L3</b>
<b>Module – 3</b>	
<p><b>Digital Modulation Techniques:</b> Phase shift Keying techniques using coherent detection: generation, detection and error probabilities of BPSK and QPSK, M-ary PSK, M-ary QAM (<b>Relevant topics in Text 1 of 7.6, 7.7</b>).</p> <p>Frequency shift keying techniques using Coherent detection: BFSK generation, detection and error probability (<b>Relevant topics in Text 1 of 7.8</b>).</p> <p>Non coherent orthogonal modulation techniques: BFSK, DPSK Symbol representation, Block diagrams treatment of Transmitter and Receiver, Probability of error (without derivation of probability of error equation) (<b>Text 1: 7.11, 7.12, 7.13</b>).</p>	<b>L1,L2,L3</b>
<b>Module-4</b>	
<p><b>Communication through Band Limited Channels:</b> Digital Transmission through Band limited channels: Digital PAM Transmission through Band limited Channels, Signal design for Band limited Channels: Design of band limited signals for zero ISI–The Nyquist Criterion (statement only), Design of band limited signals with controlled ISI–Partial Response signals, Probability of error for detection of Digital PAM: Probability of error for detection of Digital PAM with Zero ISI, Symbol-by-Symbol detection of data with controlled ISI (<b>Text 2: 9.1, 9.2, 9.3.1, 9.3.2</b>).</p> <p>Channel Equalization: Linear Equalizers (ZFE, MMSE), (<b>Text 2: 9.4.2</b>).</p>	<b>L1,L2,L3</b>
<b>Module-5</b>	
<p><b>Principles of Spread Spectrum:</b> Spread Spectrum Communication Systems: Model of a Spread Spectrum Digital Communication System, Direct Sequence Spread Spectrum Systems, Effect of De-spreading on a narrowband Interference, Probability of error (statement only), Some applications of DS Spread Spectrum Signals, Generation of PN Sequences, Frequency Hopped Spread Spectrum, CDMA based on IS-95 (<b>Text 2: 11.3.1, 11.3.2, 11.3.3, 11.3.4, 11.3.5, 11.4.2</b>).</p>	<b>L1,L2,L3</b>

<p><b>Course Outcomes:</b> At the end of the course, the students will be able to:</p> <ul style="list-style-type: none"> <li>• Develop the concepts of Band pass sampling to well specified signals and channels.</li> <li>• Utilize the performance parameters and transfer rates for low pass and bandpass symbol under ideal and corrupted non band limited channels.</li> <li>• Identify valid symbol processing and performance parameters at the receiver under ideal and corrupted bandlimited channels.</li> <li>• Identify the bandpass signals when subjected to corruption and distortion during transmission over a bandlimited channel.</li> <li>• Identify the need for data security using spread spectrum technique and error rate calculation.</li> </ul>	
<ul style="list-style-type: none"> <li>• Test and validate symbol processing and performance parameters at the receiver under ideal and corrupted bandlimited channels.</li> <li>• Demonstrate that bandpass signals subjected to corruption and distortion in a bandlimited channel can be processed at the receiver to meet specified performance criteria.</li> </ul>	
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>	
<p><b>Text Books:</b></p> <ol style="list-style-type: none"> <li>1. Simon Haykin, "Digital Communication Systems", John Wiley &amp; sons, First Edition, 2014, ISBN 978-0-471-64735-5.</li> <li>2. John G Proakis and Masoud Salehi, "Fundamentals of Communication Systems", 2014 Edition, Pearson Education, ISBN 978-8-131-70573-5.</li> </ol>	
<p><b>Reference Books:</b></p> <ol style="list-style-type: none"> <li>1. B.P. Lathi and Zhi Ding, "Modern Digital and Analog Communication Systems", Oxford University Press, 4<sup>th</sup> Edition, 2010, ISBN: 978-0-198-07380-2.</li> <li>2. Ian A Glover and Peter M Grant, "Digital Communications", Pearson Education, Third Edition, 2010, ISBN 978-0-273-71830-7.</li> <li>3. Bernard Sklar and Ray, "Digital Communications - Fundamentals and Applications", Pearson Education, Third Edition, 2014, ISBN: 978-81-317-2092-9.</li> </ol>	

<b>B. E. (EC / TC)</b> <b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI</b>			
<b>EMBEDDED SYSTEMS</b>			
<b>Course Code</b>	<b>18EC62</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03+2 (Tutorial)</b>	<b>SEE Marks</b>	<b>60</b>
		<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 04</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Explain the architectural features and instructions of 32-bit microcontroller-ARM Cortex M3.</li> <li>• Develop Programs using the various instructions of ARM Cortex M3 and C language for different applications.</li> <li>• Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.</li> <li>• Develop the hardware software co-design and firmware design approaches.</li> <li>• Explain the need of real time operating system for embedded system applications.</li> </ul>			
<b>Module1</b>			<b>RBT Level</b>
<p><b>ARM-32 bit Microcontroller:</b> Thumb-2 technology and applications of ARM, Architecture of ARM Cortex M3, Various Units in the architecture, Debugging support, General Purpose Registers, Special Registers, exceptions, interrupts, stack operation, reset sequence (Text 1: Ch-1, 2, 3)</p>			<b>L1,L2</b>
<b>Module2</b>			
<p><b>ARM Cortex M3 Instruction Sets and Programming:</b> Assembly basics, Instruction list and description, Thumb and ARM instructions, Special instructions, Useful instructions, CMSIS, Assembly and C language Programming (Text 1: Ch-4, Ch-10.1 to 10.6)</p>			<b>L1,L2, L3</b>
<b>Module3</b>			
<p><b>Embedded System Components:</b> Embedded Vs General computing system, Classification of Embedded systems, Major applications and purpose of ES. Elements of an Embedded System (Block diagram and explanation), Differences between RISC and CISC, Harvard and Princeton, Big and Little Endian formats, Memory (ROM and RAM types), Sensors, Actuators, Optocoupler, Communication Interfaces (I2C, SPI, IrDA, Bluetooth, Wi-Fi, Zigbee only) (Text 2: All the Topics from Ch-1 and Ch-2 (Fig and explanation before 2.1) 2.1.1.6 to 2.1.1.8, 2.2 to 2.2.3, 2.3 to 2.3.2, 2.3.3, selected topics of 2.4.1 and 2.4.2 only).</p>			<b>L1,L2</b>
<b>Module4</b>			
<p><b>Embedded System Design Concepts:</b> Characteristics and Quality Attributes of Embedded Systems, Operational and non-operational quality attributes, Embedded Systems-Application and Domain specific, Hardware Software Co-Design and Program Modeling (excluding UML), Embedded firmware design and development (excluding C language). Text 2: Ch-3, Ch-4 (4.1, 4.2.1 and 4.2.2 only), Ch-7 (Sections 7.1, 7.2 only), Ch-9 (Sections 9.1, 9.2, 9.3.1, 9.3.2 only)</p>			<b>L1,L2, L3</b>
<b>Module5</b>			
<p><b>RTOS and IDE for Embedded System Design:</b> Operating System basics, Types of operating systems, Task, process and threads (Only POSIX Threads with an example program), Thread preemption, Preemptive Task scheduling techniques, Task Communication, Task synchronization issues – Racing and Deadlock, Concept of Binary and counting semaphores (Mutex example without any program), How to choose an RTOS, Integration and testing of Embedded hardware and firmware, Embedded system Development Environment – Block diagram (excluding Keil), Disassembler/decompiler, simulator, emulator and debugging techniques (Text 2: Ch-10 (Sections 10.1, 10.2, 10.3, 10.5.2, 10.7, 10.8.1.1, 10.8.1.2, 10.8.2.2, 10.10 only), Ch-12, Ch-13 (a block diagram before 13.1, 13.3, 13.4, 13.5, 13.6 only)</p>			<b>L1,L2, L3</b>

**Course Outcomes:** After studying this course, students will be able to:

- Construct the architectural features and instructions of 32 bit microcontroller ARM Cortex M3.
- Make use of the knowledge gained for Programming ARM Cortex M3 for different applications.
- Identify the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.
- Develop the hardware/software co-design and firmware design using ARM Cortex M3.Instruction set.
- Analyze the need of real time operating system for embedded system applications.

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Books:**

1. Joseph Yiu, "The Definitive Guide to the ARM Cortex-M3", 2<sup>nd</sup> Edition, Newnes, (Elsevier), 2010.
2. Shibu KV, "Introduction to Embedded Systems", Tata McGraw Hill Education Private Limited, 2<sup>nd</sup> Edition.

**Reference Books:**

1. James K. Peckol, "Embedded systems - A contemporary design tool", John Wiley, 2008, ISBN: 978-0-471-72180-2.
2. Yifeng Zhu, "Embedded Systems with Arm Cortex-M Microcontrollers in Assembly Language and C", 2nd Edition - Man Press LLC ©2015 ISBN: 09826926339780982692639.
3. Embedded real time systems by K. V. K. K Prasad, Dreamtech publications, 2003.
4. Embedded Systems by Rajkamal, 2nd Edition, McGraw hill Publications, 2010.

**B. E. (EC / TC)**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI**

**MICROWAVE and ANTENNAS**

<b>Course Code</b>	<b>18EC63</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03+02(Tutorial)</b>	<b>SEE Marks</b>	<b>60</b>
		<b>Exam Hours</b>	<b>03</b>

**CREDITS – 04**

**Course Learning Objectives:** This course will enable students to:

- Describe the microwave properties and its transmission media
- Describe microwave devices for several applications
- Understand the basics of antenna theory
- Select antennas for specific applications

<b>Module1</b>	<b>RBT Level</b>
<p><b>Microwave Tubes:</b> Introduction, Reflex Klystron Oscillator, Mechanism of Oscillations, Modes of Oscillations, Mode Curve (Qualitative Analysis only). <b>(Text 1: 9.1, 9.2.1)</b></p> <p><b>Microwave Transmission Lines:</b> Microwave Frequencies, Microwave devices, Microwave Systems, Transmission Line equations and solutions, Reflection Coefficient and Transmission Coefficient, Standing Wave and Standing Wave Ratio, Smith Chart, Single Stub matching. <b>(Text 2: 0.1, 0.2, 0.3, 3.1, 3.2, 3.3, 3.5, 3.6 Except Double stub matching)</b></p>	<b>L1,L2</b>
<b>Module2</b>	
<p><b>Microwave Network theory:</b> Introduction, Symmetrical Z and Y-Parameters for reciprocal Networks, S matrix representation of Multi-Port Networks. <b>(Text1: 6.1, 6.2, 6.3)</b></p> <p><b>Microwave Passive Devices:</b> Coaxial Connectors and Adapters, Attenuators, Phase Shifters, Waveguide Tees, Magic tees. <b>(Text 1: 6.4.2, 6.4.14, 6.4.15, 6.4.16)</b></p>	<b>L1,L2</b>
<b>Module3</b>	
<p><b>Strip Lines:</b> Introduction, Micro Strip lines, Parallel Strip lines, Coplanar Strip lines, Shielded Strip Lines. <b>(Text 2: 11.1, 11.2, 11.3, 11.4)</b></p> <p><b>Antenna Basics:</b> Introduction, Basic Antenna Parameters, Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity and Gain, Antenna Apertures, Effective Height, Radio Communication Link, Antenna Field Zones. <b>(Text 3: 2.1 - 2.7, 2.9 – 2.11, 2.13)</b></p>	<b>L1,L2,L3</b>
<b>Module4</b>	
<p><b>Point Sources and Arrays:</b> Introduction, Point Sources, Power Patterns, Power Theorem, Radiation Intensity, Arrays of two isotropic point sources, Linear Arrays of n Isotropic Point Sources of equal Amplitude and Spacing. <b>(Text 3: 5.1 – 5.6, 5.9, 5.13)</b></p> <p><b>Electric Dipoles:</b> Introduction, Short Electric Dipole, Fields of a Short Dipole, Radiation Resistance of a Short Electric Dipole, Thin Linear Antenna (Field Analyses) <b>(Text 3: 6.1 - 6.5)</b></p>	<b>L1,L2,L3, L4</b>
<b>Module5</b>	
<p><b>Loop and Horn Antenna:</b> Introduction, Small loop, The Loop Antenna General Case, The Loop Antenna as a special case, Radiation resistance of loops, Directivity of Circular Loop Antennas with uniform current, Horn antennas Rectangular Horn Antennas. <b>(Text 3: 7.1, 7.2, 7.4, 7.6, 7.7, 7.8, 7.19, 7.20)</b></p> <p><b>Antenna Types:</b> The Helix geometry, Helix modes, Practical Design considerations for the mono-filar axial mode Helical Antenna, Yagi-Uda array, Parabolic reflector <b>(Text 3: 8.3, 8.4, 8.5, 8.8, 9.5)</b></p>	<b>L1,L2,L3</b>

**Course outcomes:** At the end of the course students will be able to:

- Apply Smith charts to find solutions to transmission line problems.
- Analyze passive microwave devices using S-parameters
- Evaluate various parameters and characteristics of the microwave strip lines and devices.
- Estimate radiation patterns and performance parameters of n-isotropic antennas
- Recommend various antenna configurations based on application

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.

**Text Books:**

1. **Microwave Engineering**–Annapurna Das, Sisir K Das, TMH, Publication, 2nd, 2010.
2. **Microwave Devices and circuits**- Samuel Y Liao, Pearson Education
3. **Antennas and Wave Propagation**- John D. Krauss, Ronald J Marhefka, Ahmad S Khan, 4<sup>th</sup> Edition, McGraw Hill Education, 2013

**Reference Books:**

1. **Microwave Engineering**-David M Pozar, John Wiley India Pvt. Ltd., 3rd Edn, 2008.
2. **Microwave Engineering** – Sushrut Das, Oxford Higher Education, 2nd Edn, 2015
3. **Antennas and Wave Propagation**–Harishand Sachidananda: Oxford University Press, 2007

<b>B. E. (EC / TC)</b>			
<b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI</b>			
<b>OPERATING SYSTEM</b>			
<b>Course Code</b>	<b>18EC641</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours /Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Understand the services provided by an operatingsystem.</li> <li>• Explain how processes are synchronized andscheduled.</li> <li>• Understanddifferentapproachesofmemorymanagementandvirtualmemorymanagement.</li> <li>• Describe the structure and organization of the filesystem</li> <li>• Understand interprocess communication and deadlocksituations.</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
<p><b>Introduction to Operating Systems</b> OS, Goals of an OS, Operation of an OS, Computational Structures, Resource allocation techniques, Efficiency, System Performance and User Convenience, Classes operating System, Batchprocessing, Multiprogramming, TimeSharingSystems, RealTimeanddistributedOperating Systems(<b>Topics from Sections 1.2, 1.3, 2.2 to 2.8 of Text</b>).</p>			<b>L1,L2</b>
<b>Module-2</b>			
<p><b>Process Management:</b> OS View of Processes, PCB, Fundamental State Transitions of a process, Threads, Kernel and User level Threads, Non-preemptive scheduling- FCFS and SRN, Preemptive Scheduling- RR and LCN, Scheduling in Unix and Scheduling in Linux (<b>Topics from Sections 3.3, 3.3.1 to 3.3.4, 3.4, 3.4.1, 3.4.2 , Selected scheduling topics from 4.2 and 4.3 , 4.6, 4.7 of Text</b>).</p>			<b>L1,L2,L3</b>
<b>Module – 3</b>			
<p><b>Memory Management:</b> Contiguous Memory allocation, Non-Contiguous Memory Allocation, Paging, Segmentation, Segmentation with paging, Virtual Memory Management, Demand Paging, VM handler, FIFO, LRU page replacement policies, Virtual memory in Unix and Linux(<b>Topics from Sections 5.5 to 5.9, 6.1 to 6.3 except Optimal policy and 6.3.1, 6.7,6.8 of Text</b>).</p>			<b>L1,L2,L3</b>
<b>Module-4</b>			
<p><b>File Systems:</b> File systems and IOCS, File Operations, File Organizations, Directory structures, FileProtection,InterfacebetweenFilesystemandIOCS,Allocationofdiskspace,Implementing fileaccess (<b>Topics from Sections 7.1 to 7.8 of Text</b>).</p>			<b>L1,L2</b>
<b>Module-5</b>			
<p><b>Message Passing and Deadlocks:</b> Overview of Message Passing, Implementing message passing, Mailboxes, Deadlocks, Deadlocks in resource allocation, Handling deadlocks, Deadlock detection algorithm, Deadlock Prevention (<b>Topics from Sections 10.1 to 10.3, 11.1 to 11.5 of Text</b>).</p>			<b>L1,L2</b>
<p><b>Course Outcomes:</b> At the end of the course, the students will be able to:</p> <ul style="list-style-type: none"> <li>• Explain the goals, structure, operation and types of operatingsystems.</li> <li>• Apply scheduling techniques to find performancefactors.</li> <li>• Explain organization of file systems andIOCS.</li> <li>• Apply suitable techniques for contiguous and non-contiguous memoryallocation.</li> <li>• Describe message passing, deadlock detection and preventionmethods.</li> </ul>			

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Book:**

Operating Systems – A concept based approach, by Dhamdhere, TMH, 2<sup>nd</sup> edition.

**Reference Books:**

1. Operating systems concepts, Silberschatz and Galvin, John Wiley India Pvt. Ltd, 5<sup>th</sup> edition, 2001.
2. Operating system – internals and design system, William Stalling, Pearson Education, 4<sup>th</sup> ed, 2006.
3. Design of operating systems, Tannanbhaum, TMH, 2001.

B. E. (EC / TC) ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI			
ARTIFICIAL NEURAL NETWORKS			
<b>Course Code</b>	<b>18EC642</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
CREDITS – 03			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Understand the basics of ANN and comparison with Humanbrain.</li> <li>• AcquireknowledgeonGeneralizationandfunctionapproximationofvariousANNarchitectures.</li> <li>• Understand reinforcement learning using neuralnetworks</li> <li>• Acquire knowledge of unsupervised learning using neuralnetworks.</li> </ul>			
Module-1			RBT
<b>Introduction:</b> Biological Neuron – Artificial Neural Model - Types of activation functions – <b>Architecture:</b> Feedforward and Feedback, Convex Sets, Convex Hull and Linear Separability, Non-Linear Separable Problem. XOR Problem, Multilayer Networks. <b>Learning:</b> Learning Algorithms, Error correction and Gradient Descent Rules, Learning objective of TLNs, Perceptron Learning Algorithm, Perceptron Convergence Theorem.			<b>L1, L2</b>
Module-2			
<b>Supervised Learning:</b> Perceptron learning and Non Separable sets, $\alpha$ -Least Mean Square Learning, MSEErrorsurface, SteepestDescentSearch, $\mu$ -LMSapproximatetogradientdescent, Application of LMS to Noise Cancelling, Multi-layered Network Architecture, Back propagation Learning Algorithm, Practical consideration of BPalgorithm.			<b>L1,L2, L3</b>
Module-3			
<b>Support Vector Machines and Radial Basis Function:</b> Learning from Examples, Statistical Learning Theory, Support Vector Machines, SVM application to Image Classification, Radial Basis Function Regularization theory, Generalized RBF Networks, Learning in RBFNs, RBF application to face recognition.			<b>L1,L2, L3</b>
Module-4			
<b>Attractor Neural Networks:</b> Associative Learning Attractor Associative Memory, Linear Associative memory, Hopfield Network, application of Hopfield Network, Brain State in a Box neuralNetwork, SimulatedAnnealing, BoltzmannMachine, BidirectionalAssociativeMemory.			<b>L1,L2, L3</b>
Module-5			
<b>Self-organization Feature Map:</b> Maximal Eigenvector Filtering, Extracting Principal Components, GeneralizedLearningLaws, VectorQuantization, Self-organizationFeatureMaps, Application of SOM, Growing NeuralGas.			<b>L1,L2, L3</b>
<b>Course Outcomes:</b> At the end of the course, students should be able to: <ul style="list-style-type: none"> <li>• Understandtheroleofneuralnetworksinengineering,artificialintelligence,andcognitivemodelling.</li> <li>• Understand the concepts and techniques of neural networks through the study of the most important neural networkmodels.</li> <li>• Evaluate whether neural networks are appropriate to a particularapplication.</li> <li>• Apply neural networks to particular application, and to know what steps to take to improve performance.</li> </ul>			

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Book:**

**Neural Networks A Classroom Approach** – Satish Kumar, McGraw Hill Education (India) Pvt. Ltd, Second Edition.

**Reference Books:**

1. **Introduction to Artificial Neural Systems**-J.M. Zurada, Jaico Publications 1994.
2. **Artificial Neural Networks**-B. Yegnanarayana, PHI, New Delhi 1998.

**B. E. (EC / TC)**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER**  
**-VI**

**DATA STRUCTURE USING C++**

Course Code	<b>18EC643</b>	IA Marks	40
Number of Lecture Hours/Week	03	Exam Marks	60
Total Number of Lecture/ Hours	40 (08 Hrs per Module)	Exam Hours	03

CREDITS – 03

**Course Learning Objectives:** This course will enable students to

- Solve the problems using object oriented approach
- Explain fundamentals of data structures and their applications essential for programming/problemsolving
- Analyze Linear Data Structures: Stack, Queues, Lists
- Analyze Non Linear Data Structures: Trees
- Assess appropriate data structure during program development/Problem Solving

**Module-1**

INTRODUCTION: C++ and its features, Data types, Variables, Operators, Expressions, Control structures, classes and Objects, Functions and parameters, function overloading, Recursion, Constructors, Destructors and Operator overloading, Inheritance, Polymorphism, Programming examples. L1, L2

**Module -2**

ARRAYS AND MATRICES: Arrays, Matrices, Special matrices, Sparse matrices.  
 POINTERS: Pointers, Dynamic memory allocation  
 LINEAR LISTS: Data objects and structures, Introduction to Linear and Non Linear data structures, Linear list data structures, Array Representation, Vector Representation, Singly Linked lists and chains. L1, L2

**Module -3**

STACKS: The abstract data types, Array Representation, Linked Representation, Applications – Parsing and Evaluation of arithmetic expressions, Parenthesis Matching & Towers of Hanoi. L1, L2, L3

**Module -4**

QUEUES: The abstract data types, Array Representation, Linked Representation, Applications - Railroad car arrangement, Priority Queues  
 HASHING: Dictionaries, Linear representation, Hash table representation. L1, L2, L3

**Module -5**

TREES: Binary trees, Properties and representation of binary trees, Common binary tree operations, Binary tree traversal the ADT binary tree, ADT binary tree and the class linked binary tree. Binary search trees operations and implementation. Heaps, Applications-Heap Sorting L1, L2, L3

**Course Outcomes:** After studying this course, students will be able to:

- Acquire knowledge of Dynamic memory allocation, Various types of data structures, operations and algorithms and Sparse matrices and Hashing
- Understand non Linear data structures trees and their applications
- Design appropriate data structures for solving computing problems
- Analyze the operations of Linear Data structures: Stack, Queue and LinkedList and their applications

**Text Book:**

1. Data structures, Algorithms, and applications in C++, Sartaj Sahni, Universities Press, 2<sup>nd</sup> Edition, 2005.

**Reference Books:**

2. Object Oriented Programming with C++, E. Balaguruswamy, TMH, 6th Edition, 2013.

<b>B. E. (EC / TC)</b> <b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI</b>			
<b>DIGITAL SYSTEM DESIGN USING VERILOG</b>			
<b>Course Code</b>	<b>18EC644</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (08 Hrs per module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<b>Course Learning Objectives:</b> This course will enable students to <ul style="list-style-type: none"> <li>• Understand the concepts of VerilogLanguage.</li> <li>• Design the digital systems as an activity in a larger systems designcontext.</li> <li>• Study the design and operation of semiconductor memories frequently used in application specific digitalsystem.</li> <li>• InspecthoweffectivelyIC’sareembeddedinpackageandassembledinPCB’sfordifferent application.</li> <li>• DesignanddiagnosisofprocessorsandI/Ocontrollersused inembeddedsystems.</li> </ul>			
<b>Module -1</b>			<b>RBT Level</b>
<b>Introduction and Methodology:</b> Digital Systems and Embedded Systems, Real-World Circuits, Models, Design Methodology (1.1, 1.3 to 1.5 of Text). <b>Combinational Basics:</b> Combinational Components and Circuits, Verification of Combinational Circuits (2.3 and 2.4 of Text). <b>Number Basics:</b> Unsigned integers, Signed Integers, Fixed point Numbers, Floating point Numbers (3.1.1, 3.2.1, 3.3.1 and 3.4). <b>Sequential Basics:</b> Sequential Datapaths and Control Clocked Synchronous Timing Methodology (4.3 up to 4.3.1, 4.4 up to 4.4.1 of Text).			<b>L1,L2, L3</b>
<b>Module -2</b>			
<b>Memories:</b> Concepts, Memory Types, Error Detection and Correction (Chap 5 of Text).			<b>L1,L2, L3</b>
<b>Module -3</b>			
<b>Implementation Fabrics:</b> Integrated Circuits, Programmable Logic Devices, Packaging and Circuit boards, Interconnection and Signal integrity (Chap 6 of Text).			<b>L1,L2, L3</b>
<b>Module -4</b>			
<b>I/O interfacing:</b> I/O devices, I/O controllers, Parallel Buses, Serial Transmission, I/O software (Chap 8 of Text).			<b>L1,L2, L3</b>
<b>Module -5</b>			
<b>Design Methodology:</b> Design flow, Design optimization, Design for test, Nontechnical Issues (Chap 10 of Text).			<b>L1,L2, L3, L4</b>
<b>Course outcomes:</b> After studying this course, students will be able to: <ul style="list-style-type: none"> <li>• Constructthecombinationalcircuits,usingdiscretegatesandprogrammablelogicdevices.</li> <li>• Describe how arithmetic operations can be performed for each kind of code, and also combinational circuits that implement arithmeticoperations.</li> <li>• Design a semiconductor memory for specific chipdesign.</li> <li>• Design embedded systems using small microcontrollers, larger CPUs/DSPs, or hard or soft processorcores.</li> <li>• Synthesize different types of I/O controllers that are used in embeddedsystem.</li> </ul>			
<b>Question paper pattern:</b> <ul style="list-style-type: none"> <li>• Examinationwillbeconductedfor100markswithquestionpapercontaining10fullquestions,eachof 20marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• Therewillbe2fullquestionsfromeachmodulecoveringallthetopicsofthemodule.</li> </ul>			

- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SE marks is 60.

**Text Book:**

Peter J. Ashenden, "Digital Design: An Embedded Systems Approach Using VERILOG", Elsevier, 2010.

**Reference Books:**

1. Ming-Bo Lin, "Digital System Designs and Practices: Using Verilog HDL and FPGAs", Wiley, 2008
2. Charles Roth, Lizy K. John, "Byeong Kil Lee Digital Systems Design Using Verilog, Cengage", Cengage, 1st Edition.
3. Donald E. Thomas, Philip R. Moorby, "The Verilog Hardware Description Language", Springer, Fifth edition.
4. Michael D. Ciletti, "Advanced Digital Design with the Verilog HDL" Pearson (Prentice Hall), Second edition.

<b>B. E. (EC / TC)</b> <b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI</b>			
<b>NANOELECTRONICS</b>			
<b>Course Code</b>	<b>18EC645</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Enhancebasicengineeringscienceand technicalknowledgeofNanoelectronics.</li> <li>• Explainbasicsoftop-downandbottom-upfabricationprocess,devicesandsystems.</li> <li>• Describe technologies involved in modern day electronicdevices.</li> <li>• Knowvariousnanostructuresofcarbon andthenatureofthecarbonbonditself.</li> <li>• Learn the photo physical properties of sensor used in generating asignal.</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
<b>Introduction:</b> Overview of nanoscience and engineering. Development milestones in microfabricationandelectronicindustry.Moore’slawandcontinuedminiaturization,Classification of Nanostructures, Electronic properties of atoms and solids: Isolated atom, Bonding between atoms, Giant molecular solids, Free electron models and energy bands, crystalline solids, Periodicity of crystal lattices, Electronic conduction, effects of nanometerlength scale,Fabrication methods: Top down processes, Bottom up processes methods for templating the growth of nanomaterials, ordering of nanosystems( <b>Text 1</b> ).			<b>L1, L2</b>
<b>Module-2</b>			
<b>Characterization:</b> Classification, Microscopic techniques, Field ion microscopy,scanning probe techniques, diffraction techniques: bulk and surface diffraction techniques ( <b>Text 1</b> ). <b>Inorganic semiconductor nanostructures:</b> overview of semiconductor physics. Quantum confinementinsemiconductor nanostructures:quantumwells,quantumwires,quantumdots,super-lattices, band offsets, electronic density of states ( <b>Text1</b> ).			<b>L1, L2</b>
<b>Module-3</b>			
<b>Fabrication techniques:</b> requirements of ideal semiconductor, epitaxial growth of quantum wells, lithography and etching, cleaved-edge over growth, growth of vicinal substrates, strain induced dots and wires, electrostatically induced dots and wires, Quantum well width fluctuations, thermally annealed quantum wells, semiconductor nanocrystals, collidal quantum dots, self-assembly techniques.( <b>Text 1</b> ). <b>Physical processes:</b> modulation doping, quantum hall effect, resonant tunneling, charging effects, ballistic carrier transport, Inter band absorption, intraband absorption, Light emission processes, phonon bottleneck, quantum confined stark effect, nonlinear effects, coherence and dephasing, characterization of semiconductor nanostructures: optical electrical and structural ( <b>Text 1</b> ).			<b>L1, L2</b>
<b>Module-4</b>			
<b>Carbon Nanostructures:</b> Carbon molecules, Carbon Clusters, Carbon Nanotubes, application of Carbon Nanotubes. ( <b>Text 2</b> )			<b>L1, L2</b>
<b>Module-5</b>			
<b>Nanosensors:</b> Introduction,WhatisSensorandNanosensors?,WhatmakesthempPossible?,Order From Chaos, Characterization, Perception, NanosensorsBased On Quantum Size Effects, Electrochemical Sensors, Sensors Based On Physical Properties, Nanobiosensors, Smart dust Sensor for the future. ( <b>Text3</b> ) <b>Applications:</b> Injection lasers, quantum cascade lasers, single-photon sources, biological tagging, optical memories, coulomb blockade devices, photonic structures, QWIP’s, NEMS, MEMS ( <b>Text 1</b> ).			<b>L1, L2</b>
<b>Course Outcomes:</b> After studying this course, students will be able to: <ul style="list-style-type: none"> <li>• Understand the principles behind Nanoscience engineering andNanoelectronics.</li> <li>• Know the effect of particles size on mechanical, thermal, optical and electrical properties of nanomaterials.</li> <li>• Knowthepropertiesofcarbonandcarbonnanotubesanditsapplications.</li> </ul>			

- Know the properties used for sensing and the use of smart dust sensors.
- Apply the knowledge to prepare and characterize nanomaterials.
- Analyse the process flow required to fabricate state-of-the-art transistor technology.

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Books:**

1. Ed Robert Kelsall, Ian Hamley, Mark Geoghegan, "Nanoscale Science and Technology", John Wiley, 2007.
2. Charles P Poole, Jr, Frank J Owens, "Introduction to Nanotechnology", John Wiley, Copyright 2006, Reprint 2011.
3. T Pradeep, "Nano: The essentials - Understanding Nanoscience and Nanotechnology", TMH.

**Reference Book:**

1. Ed William A Goddard III, Donald W Brenner, Sergey E. Lyshevski, Gerald J Iafrate, "Hand Book of Nanoscience Engineering and Technology", CRC press, 2003.

**B. E. ECE**  
**Choice Based Credit System (CBCS) and Outcome Based Education (OBE)**  
**SEMESTER – VI**

**PYTHON APPLICATION PROGRAMMING**

Subject Code	<b>18EC 646</b>	IA Marks	20
Number of Lecture Hours/Week	3	Exam Marks	80
Total Number of Lecture Hours	40	Exam Hours	03

**CREDITS – 03**

**Course Learning Objectives:** This course will enable students to

- Learn Syntax and Semantics and create Functions in Python.
- Handle Strings and Files in Python.
- Understand Lists, Dictionaries and Regular expressions in Python.
- Implement Object Oriented Programming concepts in Python
- Build Web Services, Network and Database Programs in Python.

<b>Module – 1</b>	<b>Teaching Hours</b>
Why should you learn to write programs, Variables, expressions and statements, Conditional execution, Functions	<b>8 Hours</b>
<b>Module – 2</b>	
Iteration, Strings, Files	<b>8 Hours</b>
<b>Module – 3</b>	
Lists, Dictionaries, Tuples, Regular Expressions	<b>8 Hours</b>
<b>Module – 4</b>	
Classes and objects, Classes and functions, Classes and methods	<b>8 Hours</b>
<b>Module – 5</b>	
Networked programs, Using Web Services, Using databases and SQL	<b>8 Hours</b>

**Course outcomes:** The students should be able to:

- Examine Python syntax and semantics and be fluent in the use of Python flow control and functions.
- Demonstrate proficiency in handling Strings and File Systems.
- Create, run and manipulate Python Programs using core data structures like Lists, Dictionaries and use Regular Expressions.
- Interpret the concepts of Object-Oriented Programming as used in Python.
- Implement exemplary applications related to Network Programming, Web Services and Databases in Python.

**Question paper pattern:**

- The question paper will have TEN questions.
- There will be TWO questions from each module.
- Each question will have questions covering all the topics under a module.
- The students will have to answer FIVE full questions, selecting ONE full question from each module.

**Text Books:**

1. Charles R. Severance, “Python for Everybody: Exploring Data Using Python 3”, 1<sup>st</sup> Edition, Create Space Independent Publishing Platform, 2016 (Chapters 1 – 13,15).
2. Allen B. Downey, "Think Python: How to Think Like a Computer Scientist”, 2<sup>nd</sup> Edition, Green Tea Press, 2015 (Chapters 15,16,17)

**References:**

1. Mark Lutz, "Programming Python", 4<sup>th</sup> Edition, O'Reilly Media, 2011. ISBN-13: 978-9350232873.
2. Wesley J Chun, "Core Python Applications Programming", 3<sup>rd</sup> Edition, Pearson Education India, 2015. ISBN-13:978-9332555365.
3. Reema Thareja, "Python Programming using problem solving approach", Oxford university press,2017

**OPEN ELECTIVES-A OFFERED BY EC/TC BOARD**

B. E. EC/TE ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI			
SIGNAL PROCESSING			
<b>Course Code</b>	<b>18EC651</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40(8Hours/Module)</b>	<b>Exam Hours</b>	<b>03</b>
CREDITS – 03			
<p><b>Course objective:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Understand,representandclassifycontinuoustimeanddiscretetimesignalsandsystems,togetherwith the representation of LTI systems.</li> <li>• Ability to represent continuous time signals (both periodic and non-periodic) in the time domain, s-domain and the frequency domain</li> <li>• Understandthepropertiesofanalogfilters,andhavetheabilitytodesignButterworthfilters</li> <li>• Understand and apply sampling theorem and convert a signal from continuous time to discrete time or from discrete time to continuous time (without loss of information)</li> <li>• Able to represent the discrete time signal in the frequency domain</li> <li>• Able to design FIR and IIR filters to meet givenspecifications</li> </ul>			
Module-1			RBT Level
Signal Definition, Signal Classification, System definition, System classification, for both continuous time and discrete time. Definition of LTI systems ( <b>Chapter 1</b> )			L1, L2
Module-2			
Introduction to Fourier Transform, Fourier Series, Relating the Laplace Transform to Fourier Transform, Frequency response of continuous time systems, ( <b>Chapter 3</b> )			L1, L2
Module-3			
Frequency response of ideal analog filters, Salient features of Butterworth filters Design and implementation of Analog Butterworth filters to meet given specifications ( <b>Chapter 8</b> )			L1,L2, L3
Module-4			
Sampling Theorem- Statement and proof, converting the analog signal to a digital signal. Practical sampling. The Discrete Fourier Transform, Properties of DFT. Comparing the frequency response of analog and digital systems. (FFT not included) ( <b>Chapter 3, 4</b> )			L1,L2, L3
Module-5			
Definition of FIR and IIR filters. Frequency response of ideal digital filters Transforming the Analog Butterworth filter to the Digital IIR Filter using suitable mapping techniques, to meet given specifications. Design of FIR Filters using the Window technique, and the frequency sampling technique to meet given specifications Comparing the designed filter with the desired filter frequency response ( <b>Chapter 8</b> )			L1,L2, L3
<p><b>Course Outcomes:</b> After studying this course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Understandandexplaincontinuoustimeanddiscretetimesignalsandsystems,intimeandfrequency domain</li> <li>• Applytheconceptsofsignalsandsystemstoobtainthedesiredparameter/representation</li> <li>• Analysethegivensystemandclassifythesystem/arriveatasuitableconclusion</li> <li>• Design analog/digital filters to meet givenspecifications</li> <li>• Design and implement the analog filter using components/ suitable simulationtools (<i>assignment component</i>)</li> <li>• Designandimplementthedigitalfilter(FIR/IIR)usingsuitablesimulationtools,andrecordtheinput and output of the filter for the given audio signal (<i>assignmentcomponent</i>)</li> </ul>			

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Book:**

‘Signals and Systems’, by Simon Haykin and Barry Van Veen, Wiley.

**References:**

1. ‘Theory and Application of Digital Signal Processing’, Rabiner and Gold
2. ‘Signals and Systems’, Schaum’s Outline series
3. ‘Digital Signal Processing’, Schaum’s Outline series

<b>B. E. EC/TC</b> <b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE)</b> <b>SEMESTER –VI</b>			
<b>SENSORS and SIGNAL CONDITIONING</b>			
<b>Course Code</b>	<b>18EC652</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>03</b>	<b>SEE marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (08 Hrs/module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Understand various technologies associated in manufacturing of sensors</li> <li>• Acquire knowledge about types of sensors used in modern digital systems</li> <li>• Get acquainted about material properties required to make sensors</li> </ul>			
<b>Module 1</b>			<b>RBT Level</b>
<b>Introduction to sensor based measurement systems:</b> General concepts and terminology, sensor classification, primary sensors, material for sensors, microsensor technology, magnetoresistors, light dependent resistors, resistive hygrometers, resistive gas sensors, liquid conductivity sensors <b>(Selected topics from ch.1 &amp; 2 of Text)</b>			<b>L1, L2</b>
<b>Module 2</b>			
<b>Reactance Variation and Electromagnetic Sensors:</b> -Capacitive Sensors, Inductive Sensors, Electromagnetic Sensors. <b>Signal Conditioning for Reactance Variation Sensors-</b> Problems and Alternatives, ac Bridges Carrier Amplifiers, Coherent Detection, Specific Signal Conditioners for Capacitive Sensors, Resolver-to-Digital and Digital-to-Resolver Converters.			<b>L1, L2</b>
<b>Module 3</b>			
<b>Self-generating Sensors-</b> Thermoelectric sensors, piezoelectric sensors, pyroelectric sensors, photovoltaic sensors, electrochemical sensors.			<b>L2,L3</b>
<b>Module 4</b>			
<b>Digital and intelligent sensors-</b> position encoders, resonant sensors, sensors based on quartz resonators, SAW sensors, Vibrating wire strain gages, vibrating cylinder sensors, Digital flow meters.			<b>L2,L3</b>
<b>Module 5</b>			
<b>Sensors based on semiconductor junctions</b> -Thermometers based on semiconductor junctions, magneto diodes and magneto transistors, photodiodes and phototransistors, sensors based on MOSFET transistors, charge- coupled sensors – types of CCD imaging sensors, ultrasonic-based sensors.			<b>L2,L3</b>
<b>Course Outcomes:</b> After studying this course, students will be able to: <ul style="list-style-type: none"> <li>• Appreciate various types of sensors and their construction</li> <li>• Use sensors specific to the end use application</li> <li>• Design systems integrated with sensors</li> </ul>			
<b>Question paper pattern:</b> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>			
<b>Text Book:</b> “Sensors and Signal Conditioning”, Ramon Pallás Areny, John G. Webster, 2nd edition, John Wiley and Sons, 2000			

**B. E. (EC / TC)  
ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER –  
VI**

**EMBEDDED SYSTEMS LAB**

<b>Course Code</b>	<b>18ECL66</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>02Hr Tutorial(Instructions) + 02 Hours Laboratory</b>	<b>SEE Marks</b>	<b>60</b>
<b>RBT Levels</b>	<b>L1, L2, L3</b>	<b>Exam Hours</b>	<b>03</b>

**CREDITS – 02**

**Course Learning Objectives:** This course will enable students to:

- Understand the instruction set of ARM Cortex M3, a 32-bit microcontroller and the software tool required for programming in Assembly and C language.
- Program ARM Cortex M3 using the various instructions in assembly language for different applications.
- Interface external devices and I/O with ARM Cortex M3.
- Develop C language programs and library functions for embedded system applications.

**Laboratory Experiments**

Conduct the following experiments on an ARM CORTEX M3 evaluation board to learn ALP and using evaluation version of Embedded 'C' & Keil uVision-4 tool/compiler.

**PART A:**

1. ALP to multiply two 16 bit binary numbers.
2. ALP to find the sum of first 10 integer numbers.
3. ALP to find the number of 0's and 1's in a 32 bit data
4. ALP to find determine whether the given 16 bit is even or odd
5. ALP to write data to RAM

**PART B:**

6. Display "Hello world" message using internal UART
7. Interface and Control the speed of a DC Motor.
8. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction.
9. Interface a DAC and generate Triangular and Square waveforms.
10. Interface a 4x4 keyboard and display the key code on an LCD.
11. Demonstrate the use of an external interrupt to toggle an LED On/Off.
12. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay.
13. Measure Ambient temperature using a sensor and SPI ADC IC.

**Course outcomes:** After studying this course, students will be able to:

- Apply the instruction set of 32 bit microcontroller ARM Cortex M3, and the software tool required for programming in Assembly and C language.
- Develop assembly language programs using ARM Cortex M3 for different applications
- Develop C language programs to interface external devices and I/O with ARM Cortex M3.
- Develop C language programs for embedded system applications.
- Develop C language programs which makes use of library functions for embedded system applications.

**Conduction of Practical Examination:**

- One Question from PART A and one Question from PART B to be asked in the examination.
- Strictly follow the instructions as printed on the cover page of answers script for breakup of marks.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

**B. E. ECE**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VI**

**COMMUNICATION LAB**

<b>Course Code</b>	<b>18ECL67</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>02Hr Tutorial(Instructions) + 02 Hours Laboratory</b>	<b>SEE Marks</b>	<b>60</b>
<b>RBT Levels</b>	<b>L1, L2, L3</b>	<b>Exam Hours</b>	<b>03</b>

**CREDITS – 02**

**Course Learning Objectives:** This course will enable students to:

- Design and test the communication circuits for different analog modulation schemes.
- Design and demonstrate the digital modulation techniques
- Demonstrate and measure the wave propagation in microstrip antennas
- Characteristics of microstrip devices and measurement of its parameters.
- Understand the probability of error computation of coherent digital modulation schemes.

**Laboratory Experiments**

**PART-A: Experiments No. 1 to 5 has to be performed using discrete components.**

1. Amplitude Modulation and Demodulation: i) Standard AM, ii) DSBSC (LM741 and LF398 ICs can be used)
2. Frequency modulation and demodulation ( IC 8038/2206 can be used)
3. Pulse sampling, flat top sampling and reconstruction
4. Time Division Multiplexing and Demultiplexing of two band limited signals.
5. FSK and PSK generation and detection
6. Measurement of frequency, guide wavelength, power, VSWR and attenuation in microwave test bench.
7. Obtain the Radiation Pattern and Measurement of directivity and gain of microstrip dipole and Yagi antennas.
8. Determination of
  - a. Coupling and isolation characteristics of microstrip directional coupler.
  - b. Resonance characteristics of microstrip ring resonator and computation of dielectric constant of the substrate.
  - c. Power division and isolation of microstrip power divider.

**PART-B: Simulation Experiments using SCILAB/MATLAB/Simulink or LabVIEW**

1. Simulate NRZ, RZ, half-sinusoid and raised cosine pulses and generate eye diagram for binary polar signaling.
2. Pulse code modulation and demodulation system.
3. Computations of the Probability of bit error for coherent binary ASK, FSK and PSK for an AWGN Channel and Compare them with their Performance curves.
4. Digital Modulation Schemes i) DPSK Transmitter and receiver, ii) QPSK Transmitter and Receiver.

**Course Outcomes:** On the completion of this laboratory course, the students will be able to:

- Make use of the characteristics and response of microwave devices
- Utilize the characteristics of microstrip antennas and measurement of its parameters.
- Construct the digital modulation schemes with the display of waveforms and computation of performance parameters
- Make use of the characteristics of Optical Fibre Communication and calculate the parameters associated with it.
- Model different digital communication concepts using simulation

**Conduct of Practical Examination:**

- All laboratory experiments are to be considered for practical examination.
- For examination one question from **PART-A** and one question from **PART-B** or only one question from **PART-B** experiments based on the complexity, to be set.
- Students are allowed to pick one experiment from the lot.
- Strictly follow the instructions as printed on the cover page of answer script for breakup of marks.
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero.

# K.S.INSTITUTE OF TECHNOLOGY, BANGALORE

(AFFILIATED TO VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM)

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.**

**WIRELESS AND CELLULAR COMMUNICATION VIII**

**Course Title: WIRELESS AND CELLULAR COMMUNICATION    Course Code : 18EC81**

**Credits:03**

**L-T-P :4-0-0**

**Contact Hours/Week: 04**

**Total Hours:50**

**Exam. Marks:100**

**IA Marks :30**

**Exam. Hours : 03**

<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>Understand the concepts of propagation over wireless channels from a physics standpoint</li> <li>Application of Communication theory both Physical and networking to understand GSM systems that handle mobile telephony</li> <li>Application of Communication theory both Physical and networking to understand CDMA systems that handle mobile telephony.</li> <li>Application of Communication theory both Physical and networking to understand LTE-4G systems.</li> </ul>	
<b>Module-1</b>	<b>RBT Level</b>
<p><b>Mobile Radio Propagation –</b>                  Large Scale Path Loss - Free Space Propagation Model, Relating Power to Electric Field, Three Basic Propagation Mechanisms – Reflection (Ground Reflection) , Diffraction, Scattering, Practical Link Budget, ( <b>Text 1 - 2.2 and Ref1 - Chapter 4</b> ).  <b>Fading and Multipath –</b> Broadband wireless channel, Delay Spread and Coherence Bandwidth, Doppler Spread and Coherence Time, Angular spread and Coherence Distance ( <b>Text 1 – 2.4</b> ) ,                  Statistical Channel Model of a Broadband Fading Channel ( <b>Text 1 – 2.5.1</b> )  <b>The Cellular Concept –</b> Cellular Concept , Analysis of Cellular Systems, Sectoring ( <b>Text 1- 2.3</b> )</p>	<b>L1, L2</b>
<b>Module-2</b>	
<p><b>GSM and TDMA Technology</b>  <b>GSM System overview –</b> Introduction, GSM Network and System Architecture, GSM Channel Concept.  <b>GSM System Operations –</b> GSM Identities, System Operations –Traffic cases, GSM Infrastructure Communications (Um Interface) ( <b>Text 2, Part1 and Part 2 of Chapter 5</b> )</p>	<b>L1,L2,L3</b>
<b>Module-3</b>	
<p><b>CDMA Technology</b>  <b>CDMA System Overview –</b> Introduction, CDMA Network and System Architecture  <b>CDMA Basics –</b> CDMA Channel Concepts, CDMA System (Layer3) operations, 3G CDMA ( <b>Text 2- Part 1, Part2 and Part 3 of Chapter6</b> )</p>	<b>L1,L2,L3</b>
<b>Module-4</b>	
<p><b>LTE – 4G</b>  <b>Key Enablers for LTE 4G –</b> OFDM, SC-FDE, SC-FDMA, Channel Dependant Multiuser Resource Scheduling, Multi-Antenna Techniques, Flat IP Architecture, LTE Network Architecture. ( <b>Text 1, Sec 1.4</b> )  <b>Multi-Carrier Modulation –</b> Multicarrier concepts, OFDM Basics, OFDM in LTE, Timing and Frequency Synchronization, Peak to Average Ratio, SC-Frequency Domain Equalization, Computational Complexity Advantage of OFDM and SC-FDE. ( <b>Text 1, Sec 3.1 – 3.7</b> )</p>	<b>L1,L2,L3</b>
<b>Module-5</b>	

<p><b>LTE - 4G</b>  <b>OFDMA and SC-FDMA</b> – Multiple Access for OFDM Systems, OFDMA, SCFDMA, Multiuser Diversity and Opportunistic Scheduling, OFDMA and SC-FDMA in LTE, OFDMA system Design Considerations.  <b>(Text 1, Sec 4.1 – 4.6)</b>  <b>The LTE Standard</b> – Introduction to LTE and Hierarchical Channel Structure of LTE, Downlink OFDMA Radio Resources, Uplink SC-FDMA Radio Resources.  <b>(Text 1, Sec 6.1 – 6.4)</b></p>	<p><b>L1, L2,L3</b></p>
<p><b>Course Outcomes:</b> After studying this course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Make use of the system architecture and the functional standard specified in LTE 4G.</li> <li>• Identify the role of the layer of LTE radio interface protocols and EPS Data convergence protocols to set up, reconfigure and release data and voice from users.</li> <li>• Utilize the UTRAN and EPS handling processes from set up to release including mobility management for a variety of data call scenarios.</li> <li>• Identify the difference between uplink , down link and the physical layer procedures that provide the services to upper layers.</li> <li>• Utilize the Performance of resource management and packet data processing and transport algorithms.</li> </ul>	
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>	
<p><b>Text Books:</b></p> <ol style="list-style-type: none"> <li>1. “Fundamentals of LTE” Arunabha Ghosh, Jan Zhang, Jefferey Andrews, Riaz Mohammed, Pearson education (Formerly Prentice Hall, Communications Engg and Emerging Technologies), ISBN-13:978-0-13-703311-9.</li> <li>2. “Introduction to Wireless Telecommunications Systems and Networks”, Gary Mullet, First Edition, Cengage Learning India Pvt Ltd., 2006, ISBN - 13: 978-81-315-0559-5.</li> </ol>	
<p><b>Reference Books:</b></p> <ol style="list-style-type: none"> <li>1. “Wireless Communications: Principles and Practice” Theodore Rappaport, 2nd Edition, Prentice Hall Communications Engineering and Emerging Technologies Series, 2002, ISBN 0- 13-042232-0.</li> <li>2. LTE for UMTS Evolution to LTE-Advanced’ Harri Holma and Antti Toskala, Second Edition- 2011, John Wiley &amp; Sons, Ltd. Print ISBN: 9780470660003.2</li> </ol>	

<b>B. E. EC/TC</b>			
<b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VIII</b>			
<b>NETWORK SECURITY</b>			
<b>Subject Code</b>	<b>18EC821</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of LectureHours</b>	<b>40 (08 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Describe network security services and mechanisms.</li> <li>• Understand Transport Level Security and Secure Socket Layer</li> <li>• Know about Security concerns in Internet Protocol security</li> <li>• Discuss about Intruders, Intrusion detection and Malicious Software</li> <li>• Discuss about Firewalls, Firewall characteristics, Biasing and Configuration</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
Attacks on Computers and Computer Security: Need for Security, Security Approaches, Principles of Security Types of Attacks. ( <b>Chapter1-Text2</b> )			<b>L1, L2</b>
<b>Module-2</b>			
Transport Level Security: Web Security Considerations, Secure Sockets Layer, Transport Layer Security, HTTPS, Secure Shell (SSH)( <b>Chapter15- Text1</b> )			<b>L1,L2</b>
<b>Module-3</b>			
IP Security: Overview of IP Security (IPSec),IP Security Architecture, Modes of Operation, Security Associations (SA), Authentication Header (AH), Encapsulating Security Payload (ESP), Internet Key Exchange. ( <b>Chapter19-Text1</b> )			<b>L1,L2</b>
<b>Module-4</b>			
Intruders, Intrusion Detection.( <b>Chapter20-Text1</b> )			<b>L1,L2</b>
<b>MALICIOUS SOFTWARE:</b> Viruses and Related Threats, Virus Countermeasures, ( <b>Chapter21-Text1</b> )			
<b>Module-5</b>			
Firewalls: The Need for firewalls, Firewall Characteristics, Types of Firewalls, Firewall Biasing, Firewall location and configuration ( <b>Chapter22-Text1</b> )			<b>L1, L2</b>
<p><b>Course Outcomes:</b> After studying this course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Explain network security services and mechanisms and explain security concepts</li> <li>• Understand the concept of Transport Level Security and Secure Socket Layer.</li> <li>• Explain Security concerns in Internet Protocol security</li> <li>• Explain Intruders, Intrusion detection and Malicious Software</li> <li>• Describe Firewalls, Firewall Characteristics, Biasing and Configuration</li> </ul>			
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>			

**TEXT BOOKS:**

1. Cryptography and Network Security Principles and Practice, Pearson Education Inc., William Stallings, 5th Edition, 2014, ISBN: 978-81-317-6166-3.
2. Cryptography and Network Security, Atul Kahate, TMH, 2003.

**REFERENCE BOOKS:**

1. Cryptography and Network Security, Behrouz A. Forouzan, TMH, 2007.

<b>B. E. EC/TC</b>			
<b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VIII</b>			
<b>MICRO ELECTROMECHANICAL SYSTEMS</b>			
<b>Course Code</b>	<b>18EC822</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of LectureHours</b>	<b>40 (08 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<b>Course Learning Objectives:</b> This course will enable students to: <ul style="list-style-type: none"> <li>• Understand overview of microsystems, their fabrication and applicationareas.</li> <li>• Working principles of several MEMSdevices.</li> <li>• Develop mathematical and analytical models of MEMSdevices.</li> <li>• Know methods to fabricate MEMSdevices.</li> <li>• Various application areas where MEMS devices can beused.</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
<b>Overview of MEMS and Microsystems:</b> MEMS and Microsystem, Typical MEMS and Microsystems Products, Evolution of Microfabrication, Microsystems and Microelectronics, Multidisciplinary Nature of Microsystems, Miniaturization. Applications and Markets.			<b>L1, L2</b>
<b>Module-2</b>			
<b>Working Principles of Microsystems:</b> Introduction, Microsensors, Microactuation, MEMS with Microactuators, Microaccelerometers, Microfluidics.			<b>L1,L2</b>
<b>Engineering Science for Microsystems Design and Fabrication:</b> Introduction, Molecular Theory of Matter and Inter-molecular Forces, Plasma Physics, Electrochemistry.			
<b>Module-3</b>			
<b>Engineering Mechanics for Microsystems Design:</b> Introduction, Static Bending of Thin Plates,MechanicalVibration,Thermomechanics,FractureMechanics,ThinFilmMechanics, Overview on Finite Element StressAnalysis.			<b>L1,L2</b>
<b>Module-4</b>			
<b>Scaling Laws in Miniaturization:</b> Introduction, Scalingin Geometry, Scaling in Rigid-Body Dynamics, Scaling in Electrostatic Forces, Scaling in Fluid Mechanics, Scaling in Heat Transfer.			<b>L1,L2</b>
<b>Module-5</b>			
<b>OverviewofMicromanufacturing:</b> Introduction,BulkMicromanufacturing,Surface Micromachining, TheLIGAProcess, Summary on Micromanufacturing.			<b>L1, L2</b>
<b>Course Outcomes:</b> After studying this course, students will be able to: <ul style="list-style-type: none"> <li>• Appreciate the technologies related to Micro Electro MechanicalSystems.</li> <li>• Understand design and fabrication processes involved with MEMSDevices.</li> <li>• AnalyzetheMEMSdevicesanddevelopsuitablemathematicalmodels.</li> </ul> Know various application areas for MEMSdevice.			
<b>Question paper pattern:</b> <ul style="list-style-type: none"> <li>• Examinationwillbeconductedfor100markswithquestionpapercontaining10fullquestions,eachof20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• Therewillbe2fullquestionsfromeachmodulecoveringallthetopicsofthemodule.</li> <li>• Studentswillhavetoanswer5fullquestions,selectingonefullquestionfromeachmodule.</li> <li>• Thetotalmarkswillbeproportionallyreducedto60 marksasSEEmarksis60.</li> </ul>			
<b>TextBook:</b> Tai-RanHsu, MEMSandMicrosystems:Design,ManufactureandNanoscaleEngineering,2 <sup>nd</sup> Ed, Wiley.			

**Reference Books:**

1. HansH.Gatzen,VolkerSaile,JurgLeuthold,MicroandNanoFabrication:ToolsandProcesses, Springer,2015.
2. DilipKumarBhattacharya,BrajeshKumarKaushik,MicroelectromechanicalSystems(MEMS), Cenage Learning.

<b>B. E. EC/TC</b>			
<b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VIII</b>			
<b>RADAR ENGINEERING</b>			
<b>Course Code</b>	<b>18EC823</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of LectureHours</b>	<b>40 (08 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Understand the Radar fundamentals and analyze the radarsignals.</li> <li>• Understandvarious technologiesinvolvedinthedesignofradartransmittersandreceivers.</li> <li>• Learn various radars like MTI, Doppler and tracking radars and theircomparison</li> </ul>			
<b>Module-1</b>			<b>RBT Level</b>
<p><b>BasicsofRadar:</b>Introduction,MaximumUnambiguousRange,RadarWaveforms, Definitions with respecttopulsewaveform-PRF,PRI,DutyCycle,PeakTransmitterPower,Averagetransmitter Power. Simple form of the Radar Equation, Radar Block Diagram and Operation, Radar Frequencies,ApplicationsofRadar,TheOriginsofRadar,IllustrativeProblems. <b>(Chapter1of Text)</b></p>			<b>L1, L2,L3</b>
<b>Module-2</b>			
<p><b>TheRadarEquation:</b>PredictionofRange`Performance,DetectionofsignalinNoise, MinimumDetectableSignal,ReceiverNoise,SNR,ModifiedRadarRangeEquation, Envelope Detector—FalseAlarmTimeandProbability,ProbabilityofDetection,RadarCrossSectionof Targets: simple targets –sphere, cone-sphere, Transmitter Power, PRF and Range Ambiguities, System Losses (qualitative treatment), IllustrativeProblems. <b>(Chapter 2 of Text, Except 2.4, 2.6, 2.8 &amp; 2.11)</b></p>			<b>L1,L2,L3</b>
<b>Module-3</b>			
<p><b>MTIandPulseDopplerRadar:</b>Introduction,Principle,DopplerFrequencyShift,SimpleCW Radar, Sweep to Sweep subtraction and Delay Line Canceler, MTI Radar with– Power Amplifier Transmitter, Delay Line Cancelers— Frequency Response of Single Delay- Line Canceler, Blind Speeds, Clutter Attenuation, MTI Improvement Factor, N- Pulse Delay-Line Canceler,DigitalMTIProcessing–Blindphases,IandQChannels,DigitalMTIDopplersignal processor, Moving Target Detector- OriginalMTD. <b>(Chapter 3: 3.1, 3.2, 3.5, 3.6 of Text)</b></p>			<b>L1,L2,L3</b>
<b>Module-4</b>			
<p><b>Tracking Radar:</b> Tracking with Radar- Types of Tracking Radar Systems, Monopulse Tracking- Amplitude Comparison Monopulse(one-and two-coordinates), Phase Comparison Monopulse. Sequential Lobing, Conical Scan Tracking, Block Diagram of Conical Scan Tracking Radar, Tracking in Range, Comparison of Trackers. <b>(Chapter4: 4.1, 4.2, 4.3 of Text)</b></p>			<b>L1,L2,L3</b>
<b>Module-5</b>			
<p><b>TheRadarAntenna:</b>FunctionsofTheRadarAntenna,AntennaParameters,ReflectorAntennasandElectronicallySteeredPhasedarrayAntennas.(Chapter9:9.1,9.29.4, 9.5 ofText) <b>Radar Receiver:</b> The Radar Receiver, Receiver Noise Figure, Super Heterodyne Receiver, Duplexers and Receivers Protectors, Radar Displays. <b>(Chapter 11 ofText)</b></p>			<b>L1, L2,L3</b>
<p><b>CourseOutcomes:</b>Attheendofthecourse,studentswillbeableto:</p> <ul style="list-style-type: none"> <li>• Understandtheradarfundamentalsandradarsignals.</li> <li>• ExplaintheworkingprincipleofpulseDopplerradars,theirapplicationsandlimitations.</li> <li>• Describe the working of various radar transmitters andreceivers.</li> <li>• Analyzetherangeparametersofpulserradarsystemwhichaffectthesystemperformance.</li> </ul>			
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examinationwillbeconductedfor100markswithquestionpapercontaining10fullquestions,eachof20 marks.</li> </ul>			

- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**TEXT BOOK:**

Introduction to Radar Systems- Merrill I Skolink, 3e, TMH, 2001

**REFERENCE BOOKS:**

1. Radar Principles, Technology, Applications—Byron Edde, Pearson Education, 2004.
2. Radar Principles—Peebles. Jr, P.Z. Wiley. New York, 1998.
3. Principles of Modern Radar: Basic Principles—Mark A. Richards, James A. Scheer, William A. Holman, Yesdee, 2013

**B. E. ECE**  
**ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VIII**

**OPTICAL COMMUNICATION NETWORKS**

<b>Course Code</b>	<b>18EC824</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	
<b>Total Number of Lecture Hours</b>	<b>40 (8 Hours / Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Learn the basic principle of optical fiber communication with different modes of light propagation.</li> <li>• Understand the transmission characteristics and losses in optical fiber.</li> <li>• Study of optical components and its applications in optical communication networks.</li> <li>• Learn the network standards in optical fiber and understand the network architectures along with its functionalities.</li> </ul>			
<b>Module -1</b>			RBT Level
<p><b>Optical fiber Communications:</b> Historical development, The general system, Advantages of optical fiber communication, Optical fiber wave guides: Ray theory transmission, Modes in planar guide, Phase and group velocity, Cylindrical fiber: Modes, Step index fibers, Graded index fibers, Single mode fibers, Cutoff wavelength, Mode field diameter, effective refractive index. Fiber Materials, Photonic crystal fibers. <b>(Text 2)</b></p>			<b>L1, L2</b>
<b>Module -2</b>			
<p><b>Transmission characteristics of optical fiber:</b> Attenuation, Material absorption losses, Linear scattering losses, Nonlinear scattering losses, Fiber bend loss, Dispersion, Chromatic dispersion, Intermodal dispersion: Multimode step index fiber.</p> <p><b>Optical Fiber Connectors:</b> Fiber alignment and joint loss, Fiber splices: Fusion Splices, Mechanical splices, Fiber connectors: Cylindrical ferrule connectors, Duplex and Multiple fiber connectors, Fiber couplers: three and four port couplers, star couplers, Optical Isolators and Circulators. <b>(Text 2)</b></p>			<b>L1, L2</b>
<b>Module -3</b>			
<p><b>Optical sources:</b> Light Emitting diodes: LED Structures, Light Source Materials, Quantum Efficiency and LED Power, Modulation. Laser Diodes: Modes and Threshold conditions, Rate equation, External Quantum Efficiency, Resonant Frequencies.</p>			<b>L1, L2</b>
<p><b>Photodetectors:</b> Physical principles of Photodiodes, Photo detector noise, Detector response time.</p> <p><b>Optical Receiver:</b> Optical Receiver Operation: Error sources, Front End Amplifiers, Receiver sensitivity, Quantum Limit. <b>(Text 1)</b></p>			
<b>Module -4</b>			
<p><b>WDM Concepts and Components:</b> Overview of WDM: Operational Principles of WDM, WDM standards, Mach-Zehnder Interferometer Multiplexers, Isolators and Circulators, Fiber grating filters, Dielectric Thin-Film Filters, Diffraction Gratings. Optical amplifiers: Basic application and Types, Semiconductor optical amplifiers, Erbium Doped Fiber Amplifiers, Raman Amplifiers, Wideband Optical Amplifiers. <b>(Text 1)</b></p>			<b>L1, L2</b>
<b>Module -5</b>			

<p><b>Optical Networks:</b> Optical network evolution and concepts: Optical networking terminology, Optical network node and switching elements, Wavelength division multiplexed networks, Public telecommunication network overview. Optical network transmission modes, layers and protocols: Synchronous networks, Asynchronous transfer mode, OSI reference model, Optical transport network, Internet protocol, Wavelength routing networks: Routing and wavelength assignment, Optical switching networks: Optical circuit switched networks, packet switched networks, Multiprotocol Label Switching, Optical burst switching networks. <b>(Text 2)</b></p>	<p><b>L1, L2</b></p>
<p><b>Course Outcomes:</b> At the end of the course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Classification and working of optical fiber with different modes of signal propagation.</li> <li>• Describe the transmission characteristics and losses in optical fiber communication.</li> <li>• Describe the construction and working principle of optical connectors, multiplexers and amplifiers.</li> <li>• Describe the constructional features and the characteristics of optical Sources and detectors.</li> <li>• Illustrate the networking aspects of optical fiber and describe various standards associated with it.</li> </ul>	
<p><b>Question paper pattern:</b></p> <ul style="list-style-type: none"> <li>• Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20 marks.</li> <li>• Each full question can have a maximum of 4 subquestions.</li> <li>• There will be 2 full questions from each module covering all the topics of the module.</li> <li>• Students will have to answer 5 full questions, selecting one full question from each module.</li> <li>• The total marks will be proportionally reduced to 60 marks as SEE marks is 60.</li> </ul>	
<p><b>Text Books:</b></p> <ol style="list-style-type: none"> <li>1. Gerd Keiser, Optical Fiber Communication, 5<sup>th</sup> Edition, McGraw Hill Education (India) Private Limited, 2015. ISBN: 1-25-900687-5.</li> <li>2. John M Senior, Optical Fiber Communications, Principles and Practice, 3<sup>rd</sup> Edition, Pearson Education, 2010, ISBN: 978-81-317-3266-3</li> </ol>	
<p><b>Reference Book:</b></p> <p>Joseph C Palais, Fiber Optic Communication, Pearson Education, 2005, ISBN: 0130085103.</p>	

<b>B. E. ECE</b>			
<b>ChoiceBasedCreditSystem(CBCS)andOutcomeBasedEducation(OBE) SEMESTER – VIII</b>			
<b>BIOMEDICAL SIGNAL PROCESSING</b>			
<b>Course Code</b>	<b>18EC825</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Lecture Hours/Week</b>	<b>3</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Lecture Hours</b>	<b>40 (8Hours/Module)</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS – 03</b>			
<p><b>Course Learning Objectives:</b> This course will enable students to:</p> <ul style="list-style-type: none"> <li>• Describe the origin, properties and suitable models of important biological signals such as ECG and EEG.</li> <li>• Know the basic signal processing techniques in analysing biological signals.</li> <li>• Acquire mathematical and computational skills relevant to the field of biomedical signal processing.</li> <li>• Describe the basics of ECG signal compression algorithms.</li> <li>• Know the complexity of various biological phenomena.</li> <li>• Understand the promises, challenges of the biomedical engineering.</li> </ul>			
<b>Module -1</b>			<b>RBT Level</b>
<p><b>Introduction to Biomedical Signals:</b> The nature of Biomedical Signals, Examples of Biomedical Signals, Objectives and difficulties in Biomedical analysis.  <b>Electrocardiography:</b> Basic electrocardiography, ECG leads systems, ECG signal characteristics.  <b>Signal Conversion :</b> Simple signal conversion systems, Conversion requirements for biomedical signals, Signal conversion circuits (<b>Text-1</b>)</p>			<b>L1,L2</b>
<b>Module -2</b>			
<p><b>Signal Averaging:</b> Basics of signal averaging, signal averaging as a digital filter, a typical averager, software for signal averaging, limitations of signal averaging.  <b>Adaptive Noise Cancelling:</b> Principal noise canceller model, 60-Hz adaptive cancelling using a sine wave model, other applications of adaptive filtering (<b>Text-1</b>)</p>			<b>L1,L2,L3</b>
<b>Module -3</b>			
<p><b>Data Compression Techniques:</b> Turning point algorithm, AZTEC algorithm, Fan algorithm, Huffman coding, data reduction algorithms The Fourier transform, Correlation, Convolution, Power spectrum estimation, Frequency domain analysis of the ECG (<b>Text-1</b>)</p>			<b>L1,L2, L3</b>
<b>Module -4</b>			
<p><b>Cardiological signal processing:</b>  Basic Electrocardiography, ECG data acquisition, ECG lead system, ECG signal characteristics (parameters and their estimation), Analog filters, ECG amplifier, and QRS detector, Power spectrum of the ECG, Bandpass filtering techniques, Differentiation techniques, Template matching techniques, A QRS detection algorithm, Real-time ECG processing algorithm, ECG interpretation, ST segment analyzer, Portable arrhythmia monitor. (<b>Text -2</b>)</p>			<b>L1,L2, L3</b>
<b>Module -5</b>			
<p><b>Neurological signal processing:</b> The brain and its potentials, The electrophysiological origin of brain waves, The EEG signal and its characteristics (EEG rhythms, waves, and transients), Correlation.  <b>Analysis of EEG channels:</b> Detection of EEG rhythms, Template matching for EEG, spike and wave detection (<b>Text-2</b>)</p>			<b>L1,L2, L3</b>
<p><b>Course Outcomes:</b> At the end of the course, students will be able to:</p> <ul style="list-style-type: none"> <li>• Possess the basic mathematical, scientific and computational skills necessary to analyse ECG and EEG signals.</li> <li>• Apply classical and modern filtering and compression techniques for ECG and EEG signals</li> <li>• Develop a thorough understanding on basics of ECG and EEG feature extraction.</li> </ul>			

**Question paper pattern:**

- Examination will be conducted for 100 marks with question paper containing 10 full questions, each of 20marks.
- Each full question can have a maximum of 4 subquestions.
- There will be 2 full questions from each module covering all the topics of the module.
- Students will have to answer 5 full questions, selecting one full question from each module.
- The total marks will be proportionally reduced to 60 marks as SEE marks is 60.

**Text Books:**

1. **Biomedical Digital Signal Processing-** Willis J. Tompkins, PHI 2001.
2. **Biomedical Signal Processing Principles and Techniques-** DC Reddy, McGraw-Hill publications 2005.

**Reference Book:**

**Biomedical Signal Analysis-** Rangaraj M. Rangayyan, John Wiley & Sons 2002.